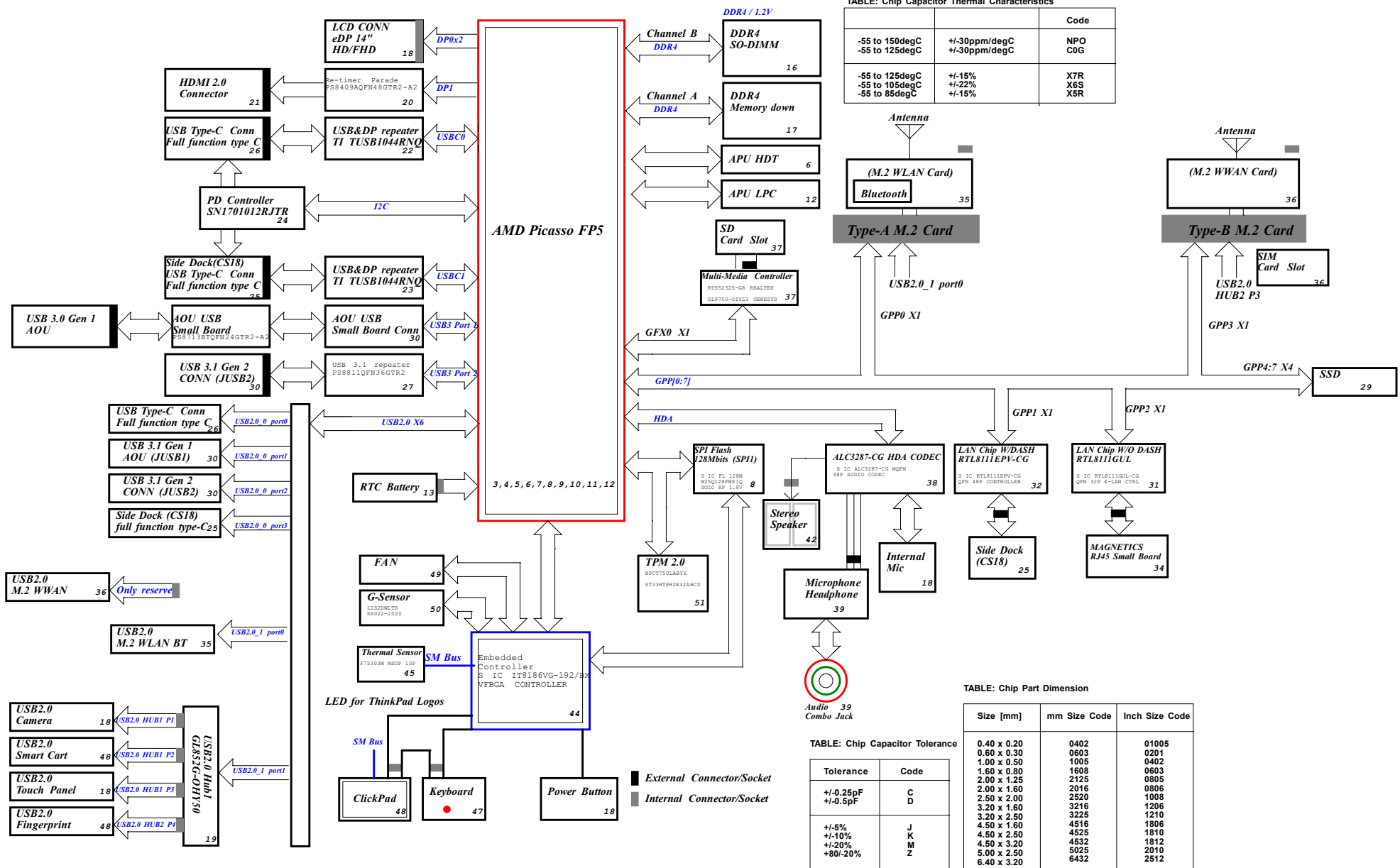


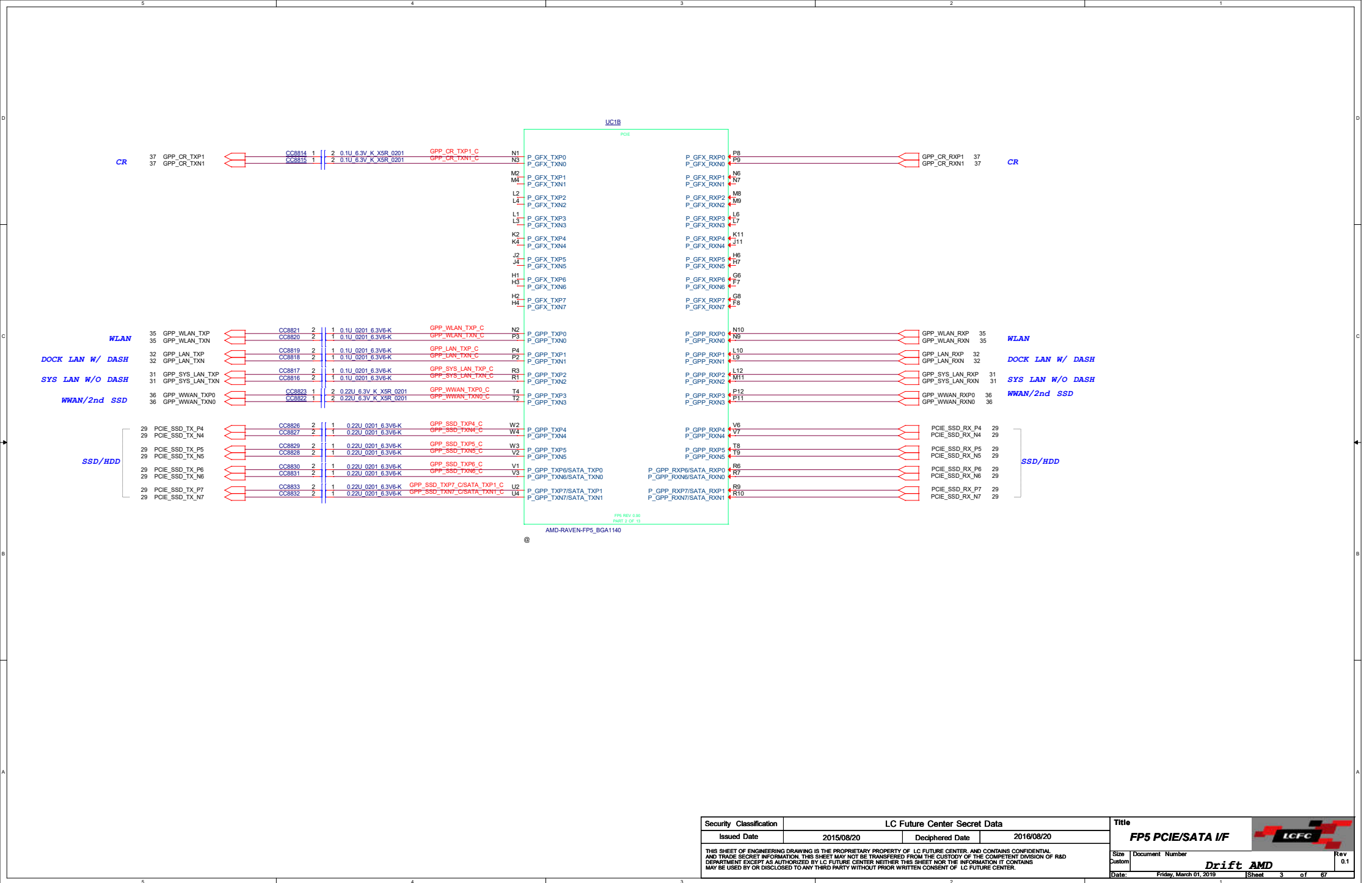
Drift AMD Logic Schematics

001_TITLE PAGE	036_M.2 SOCKET WWAN
002_BLOCK DIAGRAM	037_MEDIA CARD CONTROLLER
003-FP5 PCIE/SATA I/F	038_AUDIO ALC3287-CG
004-FP5 Memory_CHB	039_AUDIO CONNECTOR
005-FP5 Memory_CHA	040_AUDIO_JACK IN_DEBUG
006-FP5 DP/JTAG/SVI2/MISC	041_AUDIO EXT MIC I/F
007-FP5 AZ/I2C/ACPI/GPIO	042_AUDIO SPEAKER
008-FP5 CLK/LPC/SD/EMMC/UART	043_AUDIO BEEP
009-FP5 USB/WIFI/CAM	044_EC_IT8186E/FX
010-FP5 POWER	045_THERMAL SENSOR
011-FP5 GND	046_CHARGE LED
012_LPC DEBUG CONN	047_KEYBOARD/TRACK POINT
013_RTC BATTERY	048_TOUCH PAD/NFC/FPR/SCR
014_SWITCH	049_FAN CONNECTOR
015_SMBUS	050_APS G-SENSOR
016_DDR4 SO DIMM CHANNEL-B	051_DISCRETE TPM 2.0
017_DDR4 MD CHANNEL-A	052_LOAD SWITCH1
018_LCD/LID/MIC/CAMERA/PWR SW	053_LOAD SWITCH2
019_USB HUB	054_OTP
020_HDMI RETIMER	055_DC-IN
021_HDMI CONNECTOR	056_BATTERY INPUT
022_TYPE-C_USB/DP redriver1	057_BATTERY CHARGER(BQ25700A)
023_DOCK_USB/DP redriver2	058_DC/DC+3VALW(TPS51393P)
024_USB PD CONNTROLLER	059_DC/DC+5VALW_PD(NB693GQ)
025_DOCKING CONNECTOR	060_DC/DC+5VALW(NB690)
026_USB_TYPE-C CONNECTOR	061+_VDD_CORE/CORENB (ISL62771)
027_USBA REDRIVER	062+_VDD_CORE /CORENB DECOUPE
028_BLANK	063_+0.9VALWP(NB693)
029_M.2 SSD CONNECTOR	064_DC/DC+1.2VP(NB687)
030_USB POWER/CONN	065_DC/DC_+1.8VALW(RT8068A)
031_SYSTEM LAN 8111GUL	066_PWR-Power Diagram
032_DOCK LAN 8111EPV	067_SCREW
033_BLANK	
034_SYSTEM LAN CONNECTOR	
035_M.2 SOCKET WLAN	

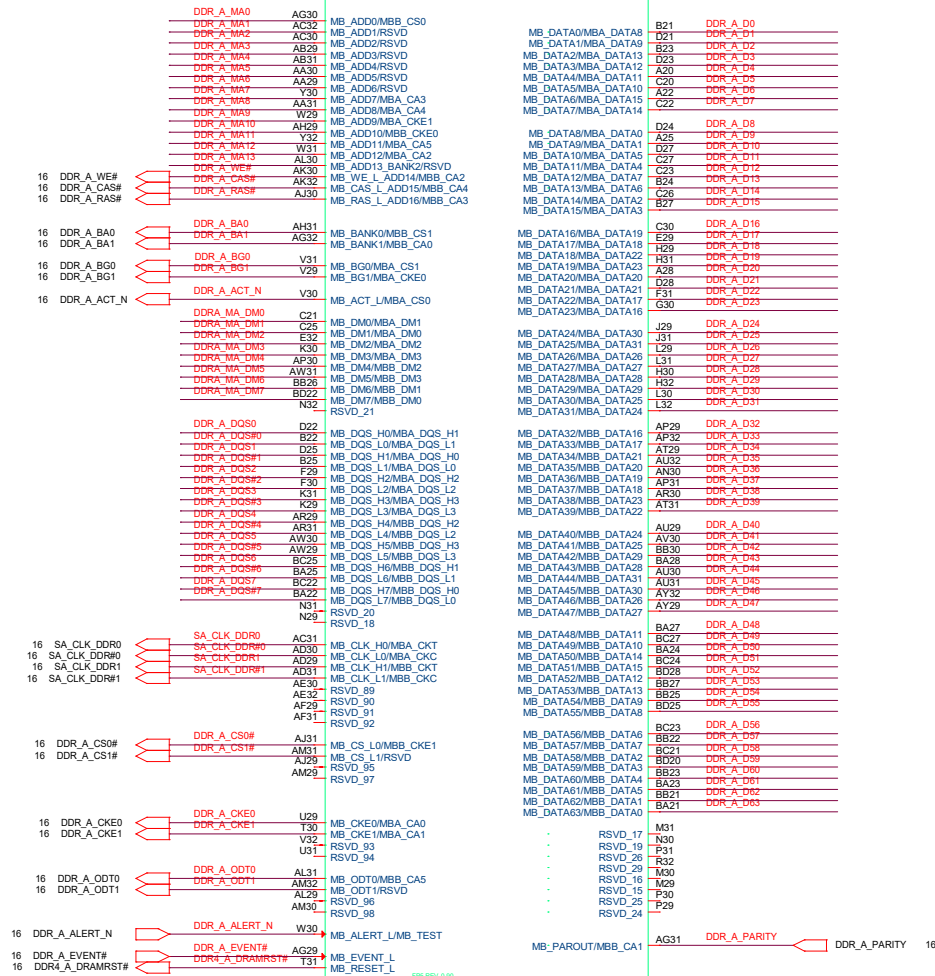
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Date: Friday, March 01, 2015			Sheet 1 of 87

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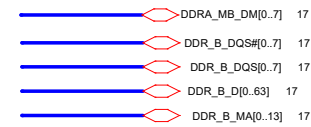
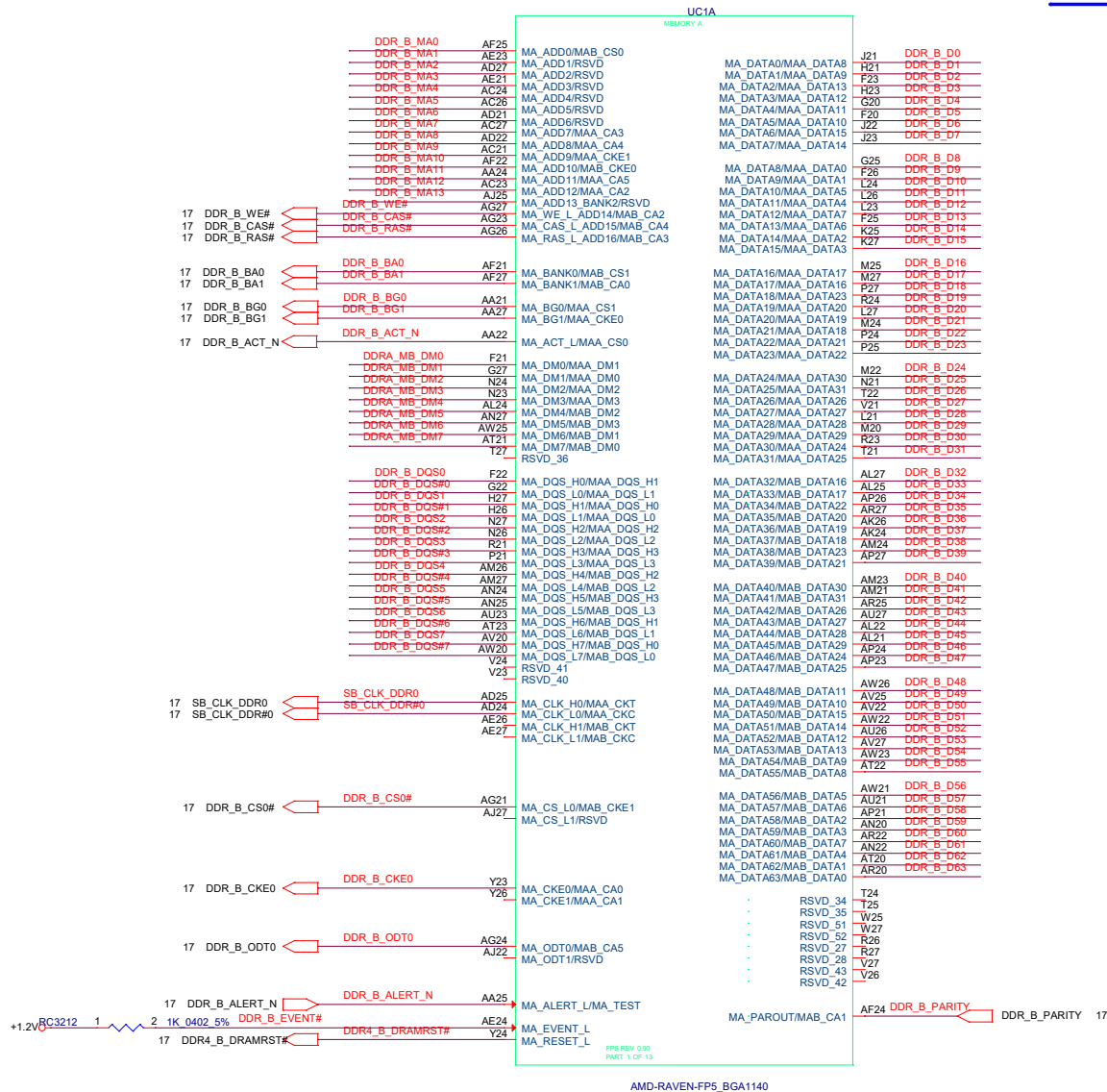


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Drift AMD					
Date:	Friday, March 01, 2019			Sheet	3 of 67

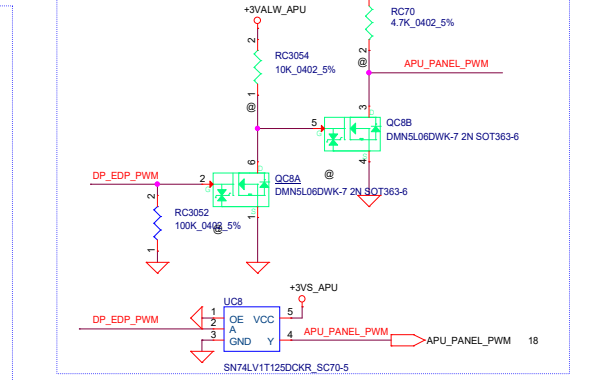
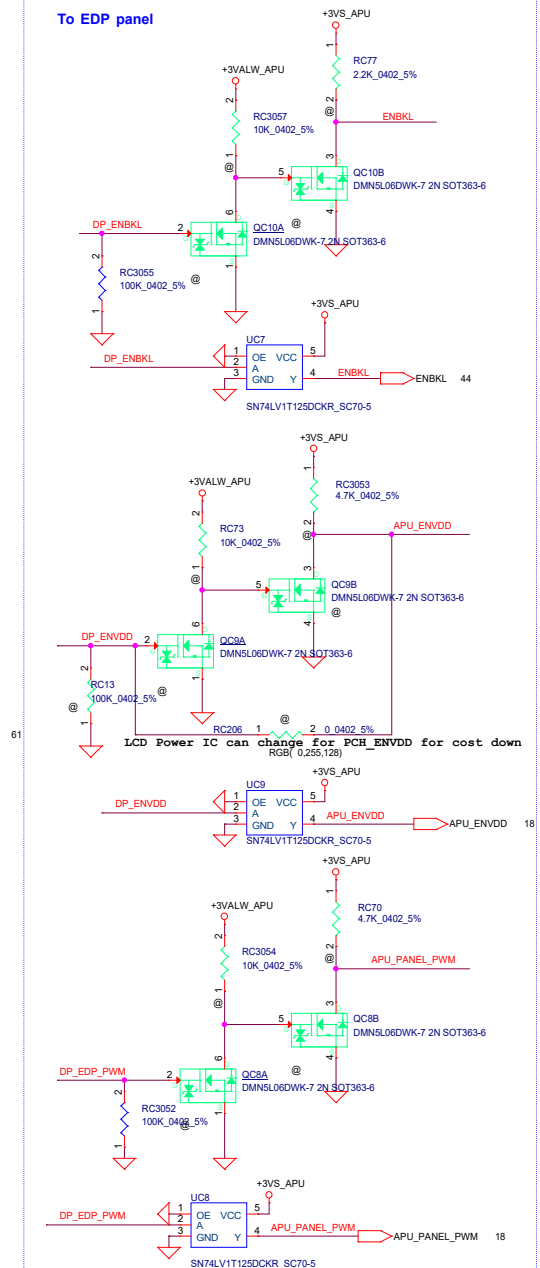
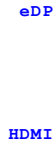



APU side			SO-DIMM side		
Net Name	Pin Name	Pin Number	Net Name	Pin Name	Pin Number
DDR A D0	MB_DATA0/MBB_DATA8	B21	DDR A D0	DQ0	8
DDR A D1	MB_DATA1/MBB_DATA9	D21	DDR A D1	DQ1	7
DDR A D2	MB_DATA2/MBB_DATA13	B23	DDR A D2	DQ2	20
DDR A D3	MB_DATA3/MBB_DATA12	D23	DDR A D3	DQ3	21
DDR A D4	MB_DATA4/MBB_DATA11	A20	DDR A D4	DQ4	4
DDR A D5	MB_DATA5/MBB_DATA10	C20	DDR A D5	DQ5	3
DDR A D6	MB_DATA6/MBB_DATA15	A22	DDR A D6	DQ6	16
DDR A D7	MB_DATA7/MBB_DATA14	C22	DDR A D7	DQ7	17
DDR A D8	MB_DATA8/MBB_DATA0	D24	DDR A D8	DQ13	25
DDR A D9	MB_DATA9/MBB_DATA1	A25	DDR A D9	DQ12	24
DDR A D10	MB_DATA10/MBB_DATA5	D27	DDR A D10	DQ15	37
DDR A D11	MB_DATA11/MBB_DATA4	C27	DDR A D11	DQ11	42
DDR A D12	MB_DATA12/MBB_DATA7	C23	DDR A D12	DQ8	28
DDR A D13	MB_DATA13/MBB_DATA6	B24	DDR A D13	DQ9	29
DDR A D14	MB_DATA14/MBB_DATA2	C26	DDR A D14	DQ14	38
DDR A D15	MB_DATA15/MBB_DATA3	B27	DDR A D15	DQ10	41
DDR A D16	MB_DATA16/MBB_DATA19	C30	DDR A D16	DQ17	49
DDR A D17	MB_DATA17/MBB_DATA18	E29	DDR A D17	DQ21	45
DDR A D18	MB_DATA18/MBB_DATA23	H29	DDR A D18	DQ19	63
DDR A D19	MB_DATA19/MBB_DATA22	H31	DDR A D19	DQ18	62
DDR A D20	MB_DATA20/MBB_DATA20	A28	DDR A D20	DQ16	50
DDR A D21	MB_DATA21/MBB_DATA21	D28	DDR A D21	DQ20	46
DDR A D22	MB_DATA22/MBB_DATA17	F31	DDR A D22	DQ22	58
DDR A D23	MB_DATA23/MBB_DATA16	G30	DDR A D23	DQ23	59
DDR A D24	MB_DATA24/MBB_DATA30	I29	DDR A D24	DQ24	70
DDR A D25	MB_DATA25/MBB_DATA31	I31	DDR A D25	DQ28	66
DDR A D26	MB_DATA26/MBB_DATA26	L29	DDR A D26	DQ30	79
DDR A D27	MB_DATA27/MBB_DATA27	L31	DDR A D27	DQ26	83
DDR A D28	MB_DATA28/MBB_DATA28	H30	DDR A D28	DQ25	71
DDR A D29	MB_DATA29/MBB_DATA29	H32	DDR A D29	DQ29	67
DDR A D30	MB_DATA30/MBB_DATA25	L30	DDR A D30	DQ31	80
DDR A D31	MB_DATA31/MBB_DATA24	L32	DDR A D31	DQ27	84
DDR A D32	MB_DATA32/MBB_DATA16	AP29	DDR A D32	DQ32	174
DDR A D33	MB_DATA33/MBB_DATA17	AP32	DDR A D33	DQ33	173
DDR A D34	MB_DATA34/MBB_DATA21	AT29	DDR A D34	DQ34	187
DDR A D35	MB_DATA35/MBB_DATA20	AU32	DDR A D35	DQ35	186
DDR A D36	MB_DATA36/MBB_DATA19	AN30	DDR A D36	DQ36	170
DDR A D37	MB_DATA37/MBB_DATA18	AP31	DDR A D37	DQ37	169
DDR A D38	MB_DATA38/MBB_DATA23	AR30	DDR A D38	DQ38	183
DDR A D39	MB_DATA39/MBB_DATA22	AT31	DDR A D39	DQ39	182
DDR A D40	MB_DATA40/MBB_DATA20	AU29	DDR A D40	DQ40	195
DDR A D41	MB_DATA41/MBB_DATA21	AY30	DDR A D41	DQ41	194
DDR A D42	MB_DATA42/MBB_DATA27	H30	DDR A D42	DQ47	204
DDR A D43	MB_DATA43/MBB_DATA28	HA28	DDR A D43	DQ43	208
DDR A D44	MB_DATA44/MBB_DATA31	AU30	DDR A D44	DQ44	191
DDR A D45	MB_DATA45/MBB_DATA30	AU31	DDR A D45	DQ45	190
DDR A D46	MB_DATA46/MBB_DATA26	AY32	DDR A D46	DQ42	207
DDR A D47	MB_DATA47/MBB_DATA27	AY29	DDR A D47	DQ46	203
DDR A D48	MB_DATA48/MBB_DATA11	BA27	DDR A D48	DQ52	211
DDR A D49	MB_DATA49/MBB_DATA10	BC27	DDR A D49	DQ49	215
DDR A D50	MB_DATA50/MBB_DATA14	BA24	DDR A D50	DQ50	228
DDR A D51	MB_DATA51/MBB_DATA13	BC24	DDR A D51	DQ55	225
DDR A D52	MB_DATA52/MBB_DATA12	BD28	DDR A D52	DQ48	216
DDR A D53	MB_DATA53/MBB_DATA11	BB27	DDR A D53	DQ53	212
DDR A D54	MB_DATA54/MBB_DATA10	BD25	DDR A D54	DQ54	224
DDR A D55	MB_DATA55/MBB_DATA15	BD25	DDR A D55	DQ51	229
DDR A D56	MB_DATA56/MBB_DATA6	BC23	DDR A D56	DQ56	245
DDR A D57	MB_DATA57/MBB_DATA7	BB22	DDR A D57	DQ57	232
DDR A D58	MB_DATA58/MBB_DATA2	BC21	DDR A D58	DQ53	246
DDR A D59	MB_DATA59/MBB_DATA3	BD20	DDR A D59	DQ57	236
DDR A D60	MB_DATA60/MBB_DATA4	BB23	DDR A D60	DQ56	237
DDR A D61	MB_DATA61/MBB_DATA5	BA23	DDR A D61	DQ61	233
DDR A D62	MB_DATA62/MBB_DATA1	BB21	DDR A D62	DQ59	250
DDR A D63	MB_DATA63/MBB_DATA0	BA21	DDR A D63	DQ58	249

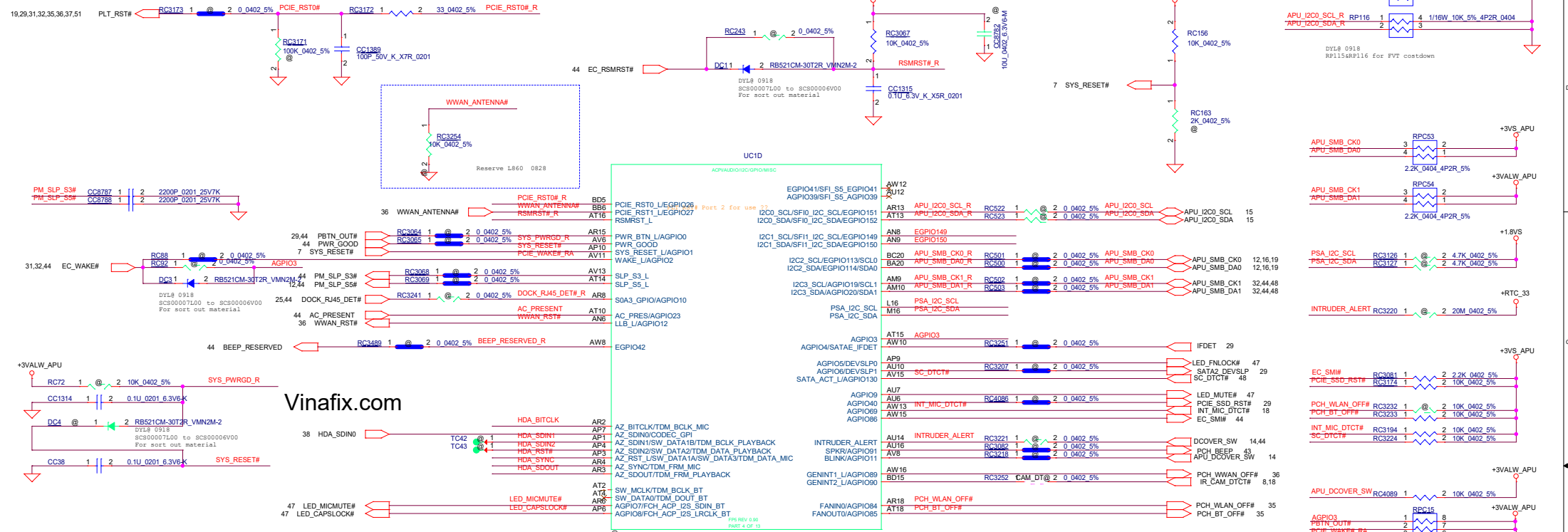
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Date:	Friday, March 01, 2019			Sheet 4 of 67	



APU side			MD side		
Net Name	Name	Number	Net Name	Pin Name	Pin Number
DDR B D0	MA DATA0/MAB DATA8	J21	DDR B D0	DQL6	13
DDR B D1	MA DATA1/MAB DATA9	H21	DDR B D1	DQL4	H2
DDR B D2	MA DATA2/MAB DATA10	F23	DDR B D2	DQL1	F7
DDR B D3	MA DATA3/MAB DATA12	H23	DDR B D3	DQL3	H7
DDR B D4	MA DATA4/MAB DATA11	G20	DDR B D4	DQL0	G2
DDR B D5	MA DATA5/MAB DATA10	F20	DDR B D5	DQL2	H3
DDR B D6	MA DATA6/MAB DATA15	J22	DDR B D6	DQL5	H8
DDR B D7	MA DATA7/MAB DATA14	J23	DDR B D7	DQL7	J7
DDR B D8	MA DATA8/MAB DATA0	G25	DDR B D8	DQL4	C2
DDR B D9	MA DATA9/MAB DATA1	F26	DDR B D9	DQL3	C7
DDR B D10	MA DATA10/MAB DATA5	L24	DDR B D10	DQL2	C3
DDR B D11	MA DATA11/MAB DATA4	L26	DDR B D11	DQL5	C8
DDR B D12	MA DATA12/MAB DATA7	L23	DDR B D12	DQL6	D3
DDR B D13	MA DATA13/MAB DATA6	F25	DDR B D13	DQL0	A3
DDR B D14	MA DATA14/MAB DATA2	K25	DDR B D14	DQL7	D7
DDR B D15	MA DATA15/MAB DATA3	K27	DDR B D15	DQL1	H8
DDR B D16	MA DATA16/MAB DATA17	M25	DDR B D16	DQL6	J3
DDR B D17	MA DATA17/MAB DATA16	M27	DDR B D17	DQL0	G2
DDR B D18	MA DATA18/MAB DATA23	P27	DDR B D18	DQL3	H7
DDR B D19	MA DATA19/MAB DATA20	R24	DDR B D19	DQL5	H8
DDR B D20	MA DATA20/MAB DATA19	L27	DDR B D20	DQL4	H2
DDR B D21	MA DATA21/MAB DATA18	M24	DDR B D21	DQL2	H3
DDR B D22	MA DATA22/MAB DATA21	P24	DDR B D22	DQL7	H7
DDR B D23	MA DATA23/MAB DATA22	P25	DDR B D23	DQL1	F7
DDR B D24	MA DATA24/MAB DATA30	M22	DDR B D24	DQL4	C2
DDR B D25	MA DATA25/MAB DATA31	N21	DDR B D25	DQL6	D3
DDR B D26	MA DATA26/MAB DATA26	T22	DDR B D26	DQL7	D7
DDR B D27	MA DATA27/MAB DATA27	V21	DDR B D27	DQL5	C8
DDR B D28	MA DATA28/MAB DATA28	L21	DDR B D28	DQL0	A3
DDR B D29	MA DATA29/MAB DATA29	M20	DDR B D29	DQL2	C3
DDR B D30	MA DATA30/MAB DATA24	R23	DDR B D30	DQL3	C7
DDR B D31	MA DATA31/MAB DATA25	T21	DDR B D31	DQL1	H8
DDR B D32	MA DATA32/MAB DATA16	AL27	DDR B D32	DQL0	G2
DDR B D33	MA DATA33/MAB DATA17	AL25	DDR B D33	DQL4	H2
DDR B D34	MA DATA34/MAB DATA22	AP26	DDR B D34	DQL3	H7
DDR B D35	MA DATA35/MAB DATA20	AK26	DDR B D35	DQL1	F7
DDR B D36	MA DATA36/MAB DATA19	AK24	DDR B D36	DQL2	H3
DDR B D37	MA DATA37/MAB DATA18	AM24	DDR B D37	DQL6	J3
DDR B D38	MA DATA38/MAB DATA21	AP27	DDR B D38	DQL5	H8
DDR B D39	MA DATA39/MAB DATA23	AM21	DDR B D39	DQL7	J7
DDR B D40	MA DATA40/MAB DATA24	AK25	DDR B D40	DQL4	C2
DDR B D41	MA DATA41/MAB DATA25	AL27	DDR B D41	DQL6	D3
DDR B D42	MA DATA42/MAB DATA26	AL22	DDR B D42	DQL1	H8
DDR B D43	MA DATA43/MAB DATA27	AL21	DDR B D43	DQL5	C8
DDR B D44	MA DATA44/MAB DATA28	AP24	DDR B D44	DQL0	A3
DDR B D45	MA DATA45/MAB DATA29	AP23	DDR B D45	DQL2	C3
DDR B D46	MA DATA46/MAB DATA30	AW26	DDR B D46	DQL3	C7
DDR B D47	MA DATA47/MAB DATA31	AV25	DDR B D47	DQL7	D7
DDR B D48	MA DATA48/MAB DATA16	AL27	DDR B D48	DQL0	G2
DDR B D49	MA DATA49/MAB DATA17	AL25	DDR B D49	DQL4	H2
DDR B D50	MA DATA50/MAB DATA18	AP26	DDR B D50	DQL6	J3
DDR B D51	MA DATA51/MAB DATA19	AK26	DDR B D51	DQL5	H8
DDR B D52	MA DATA52/MAB DATA20	AK24	DDR B D52	DQL4	H2
DDR B D53	MA DATA53/MAB DATA21	AM24	DDR B D53	DQL6	J3
DDR B D54	MA DATA54/MAB DATA22	AP27	DDR B D54	DQL7	J7
DDR B D55	MA DATA55/MAB DATA23	AM21	DDR B D55	DQL4	C2
DDR B D56	MA DATA56/MAB DATA24	AK25	DDR B D56	DQL6	D3
DDR B D57	MA DATA57/MAB DATA25	AL27	DDR B D57	DQL1	H8
DDR B D58	MA DATA58/MAB DATA26	AL22	DDR B D58	DQL5	C8
DDR B D59	MA DATA59/MAB DATA27	AL21	DDR B D59	DQL0	A3
DDR B D60	MA DATA60/MAB DATA28	AP24	DDR B D60	DQL2	C3
DDR B D61	MA DATA61/MAB DATA29	AP23	DDR B D61	DQL4	H2
DDR B D62	MA DATA62/MAB DATA30	AW26	DDR B D62	DQL7	D7
DDR B D63	MA DATA63/MAB DATA31	AV25	DDR B D63	DQL3	C7

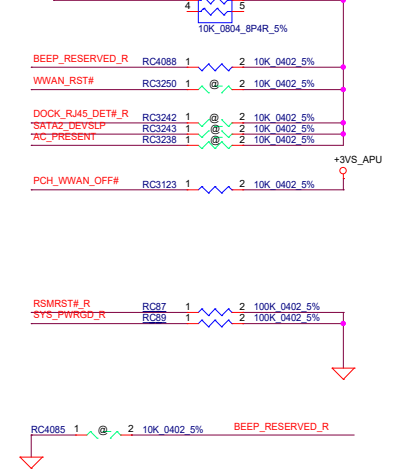
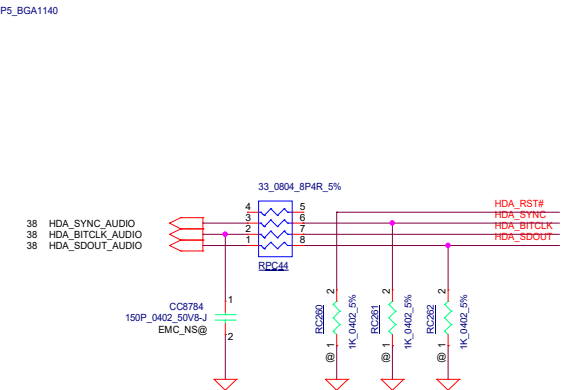
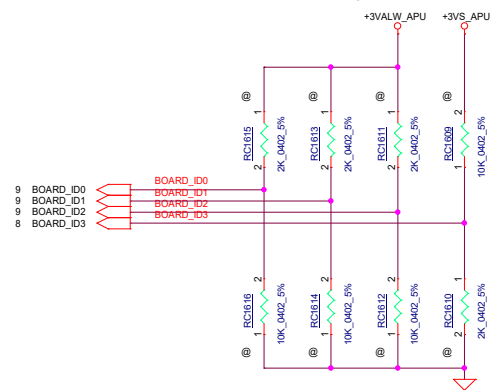


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				Rev	0.1

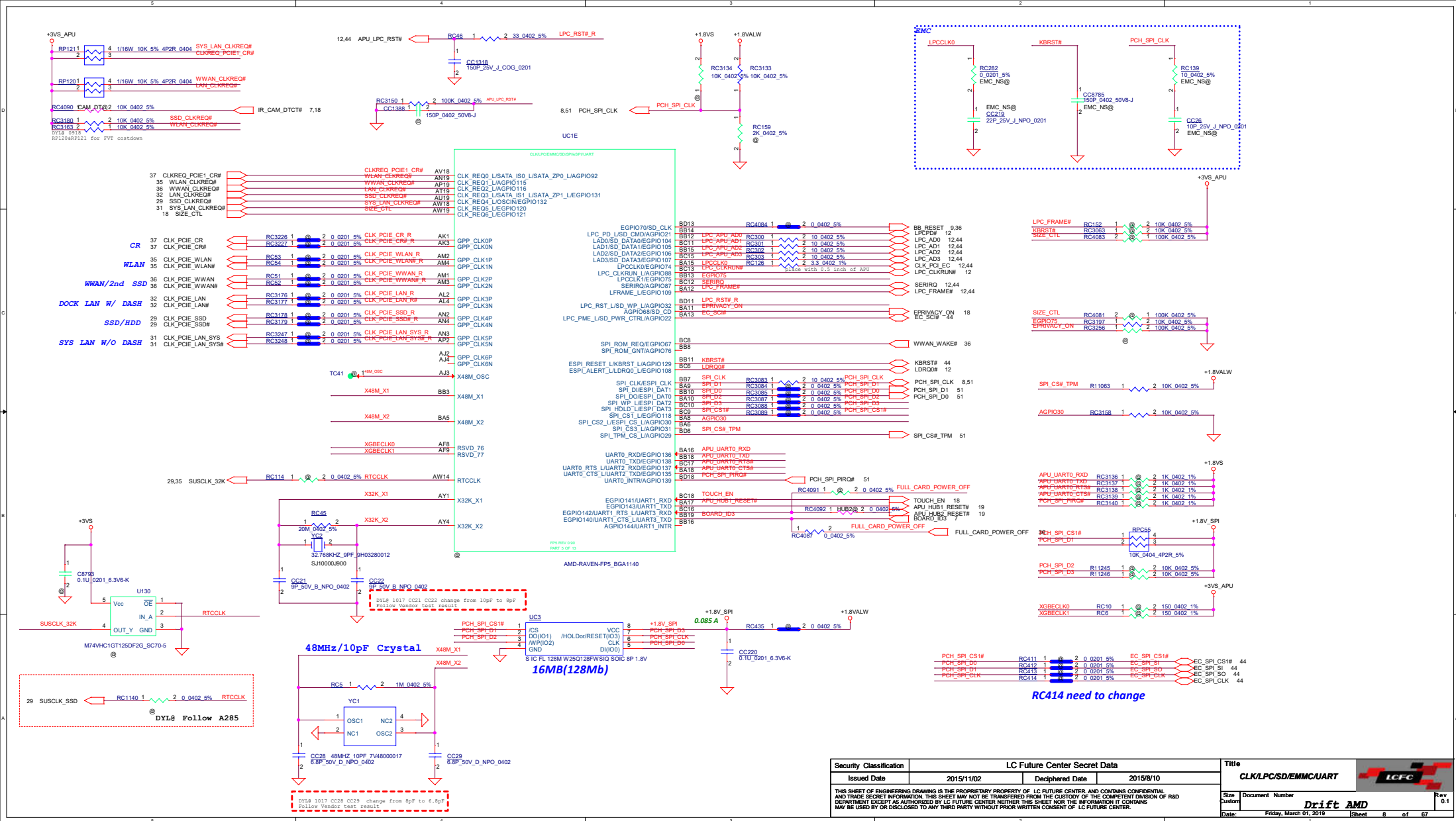


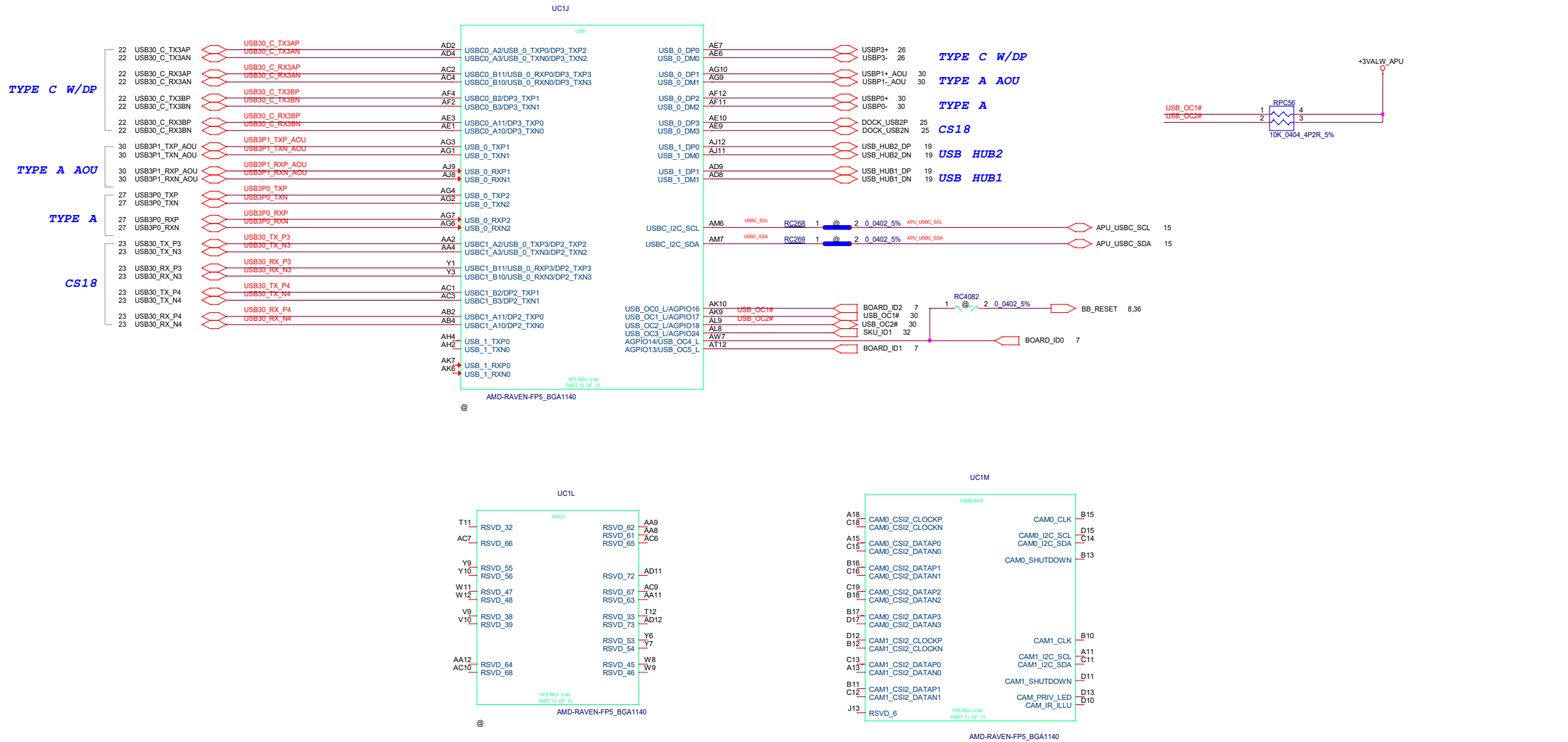
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DYL@ 0831 Need to check				
Board ID		Description	Stuff R	
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	001	Hynix 8Gb 2400 MT/s	RC1612	RC1614 RC1615
	010	Reserved	RC1612	RC1613 RC1616
	011	Samsung 16Gb 2400 MT/s	RC1612	RC1613 RC1615
	100	Hynix 16Gb 2400 MT/s	RC1611	RC1614 RC1616
	101	Micron 16Gb 2400 MT/s	RC1611	RC1614 RC1615
	110	Reserved	RC1611	RC1613 RC1616
Board_ID3	0	Touch Panel	RC1610	
	1	NON-Touch Panel	RC1609	
Board_ID4	0	FP	RC1607	
	1	NON-FP	RC1608	
Board_ID5	0	Reserved	RC123	
	1	Reserved	RC1606	

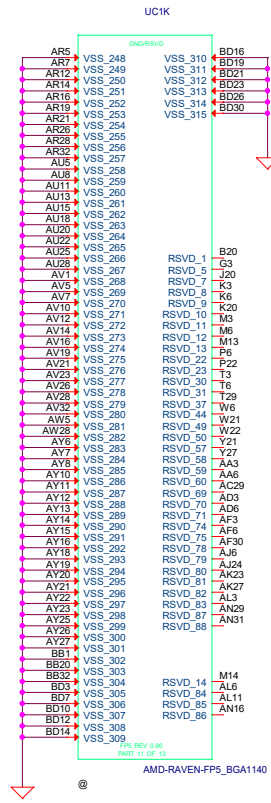
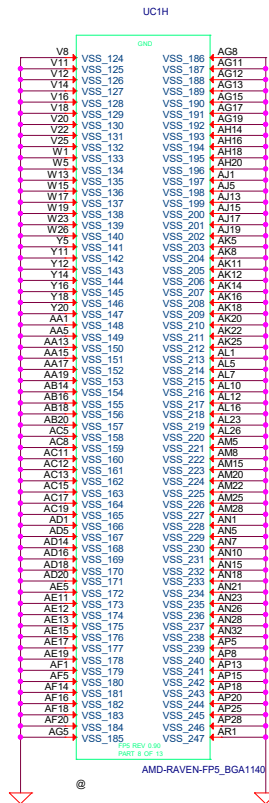
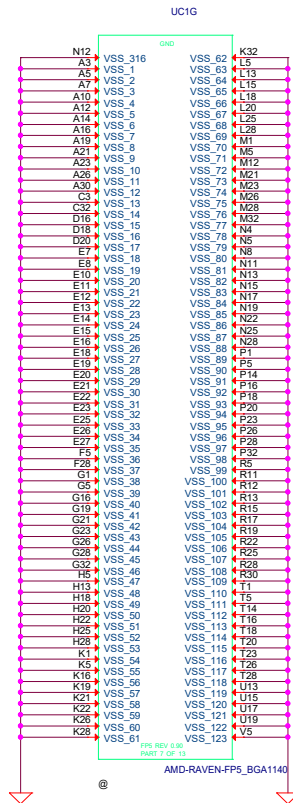


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				Size	Document Number
Date:	Friday, March 01, 2019	Sheet	9 of 67		



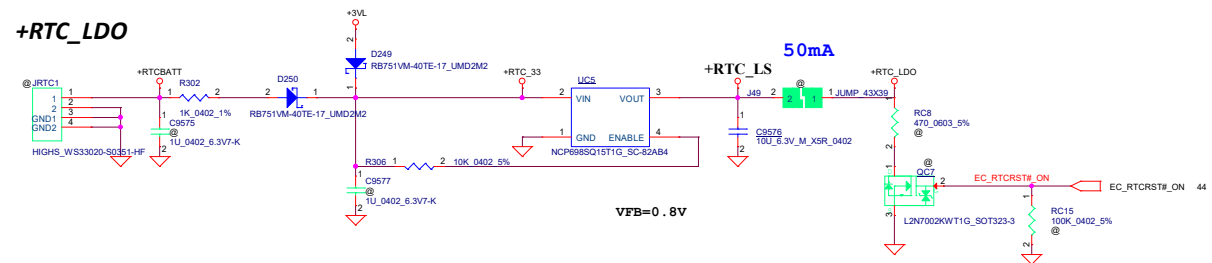
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Date:		Friday, March 01, 2019		ISheet	11 of 67



Rev

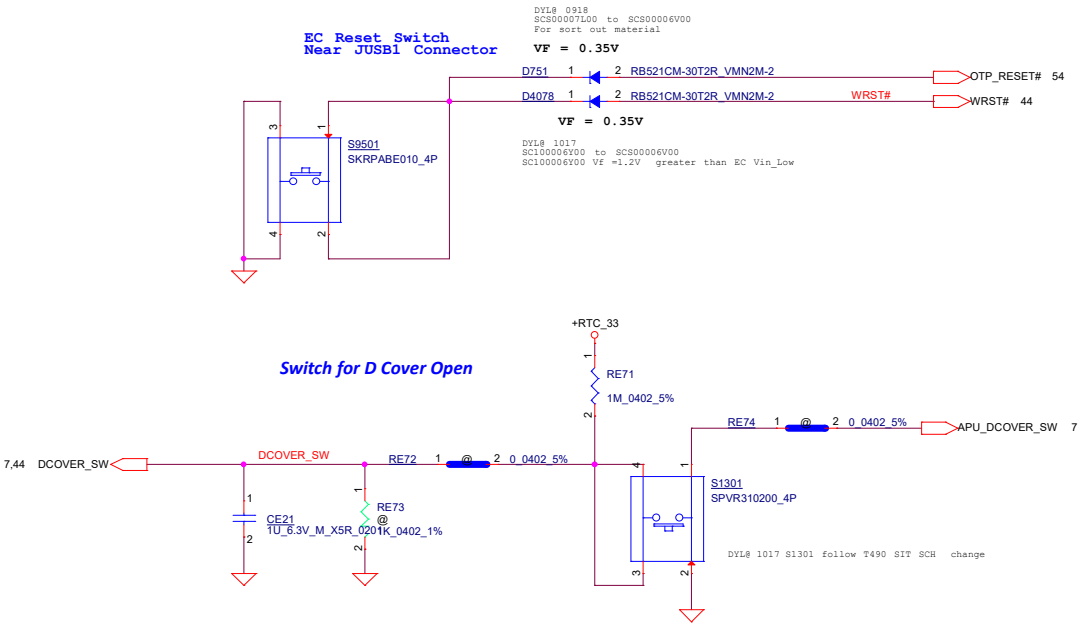
0.1


+RTC_LDO

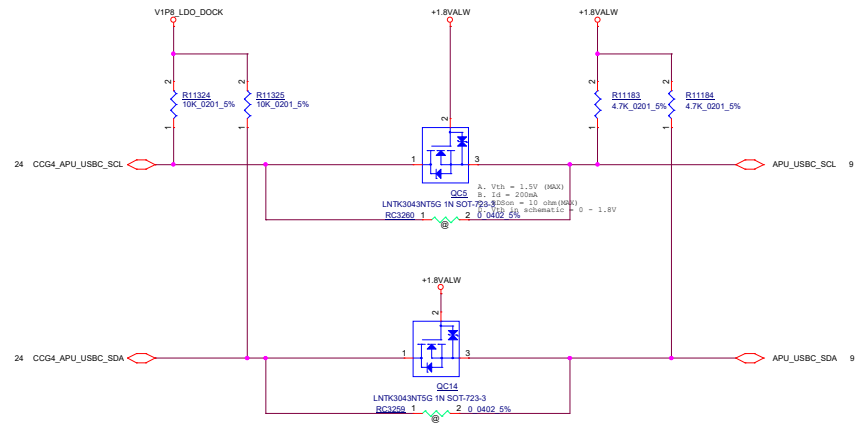
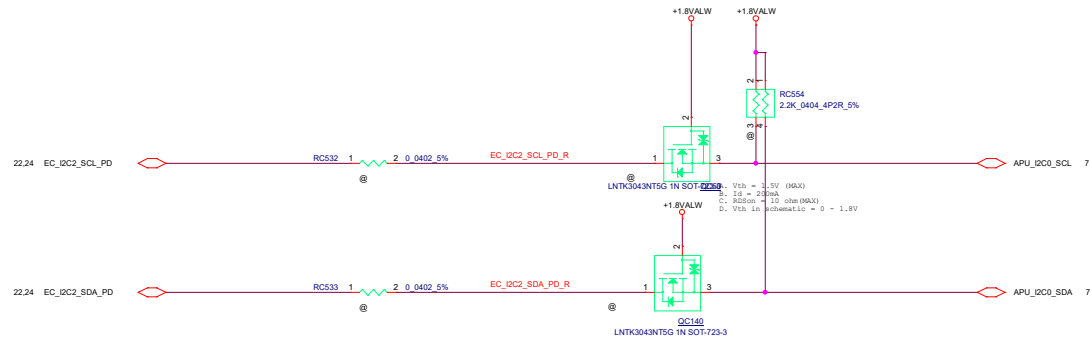


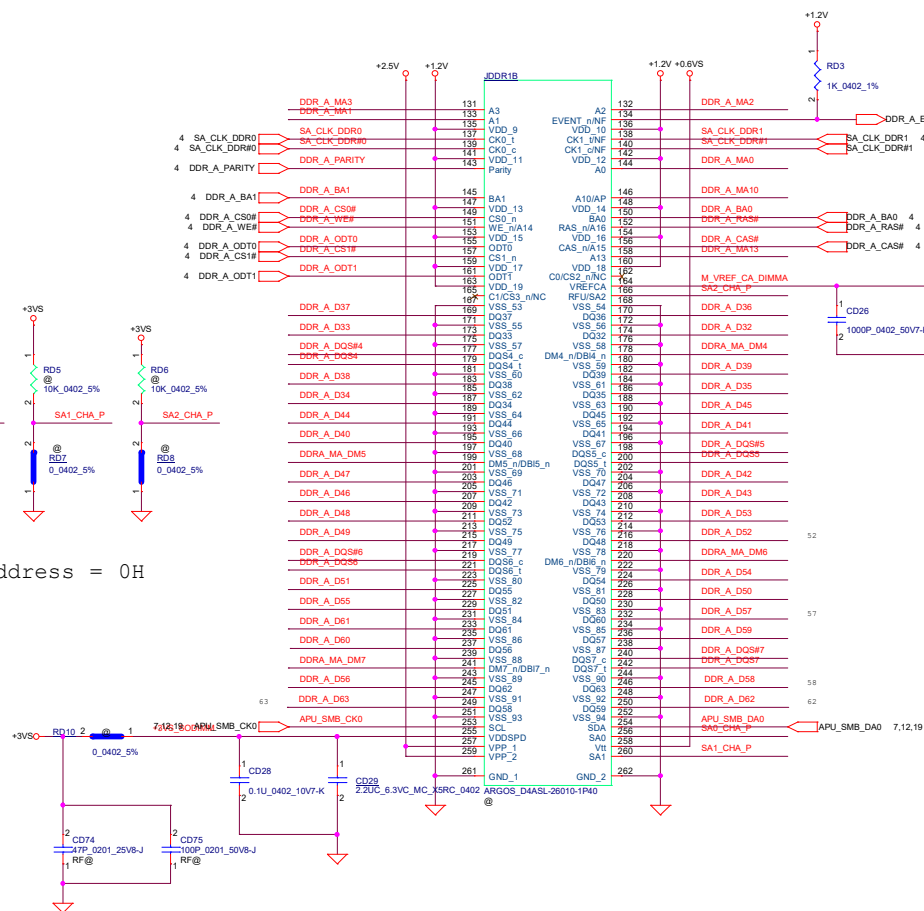
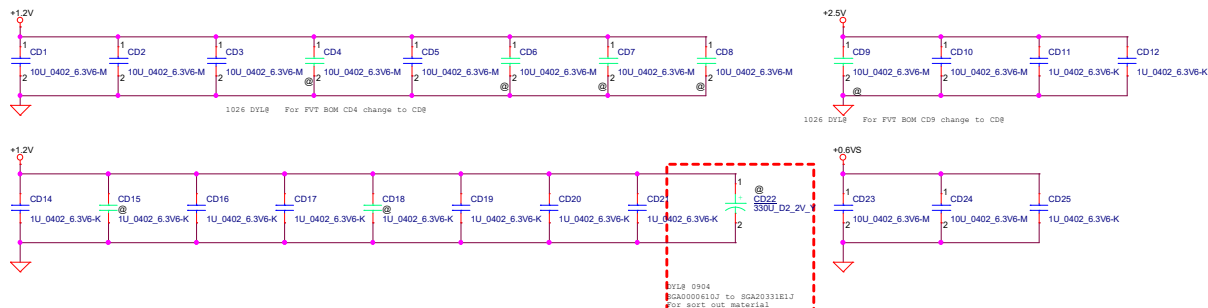
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				Sheet	13 of 67

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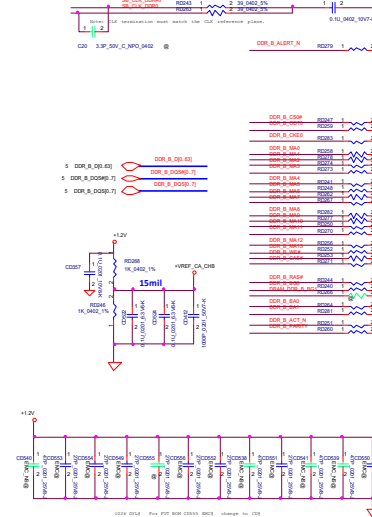
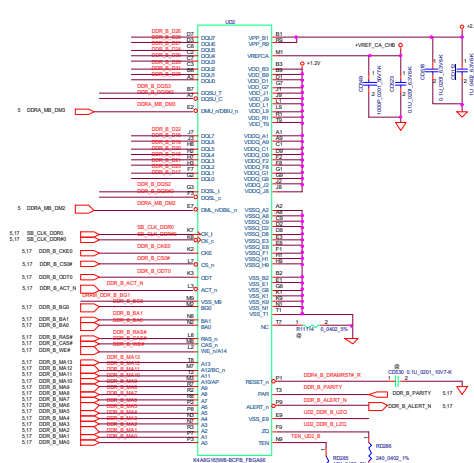
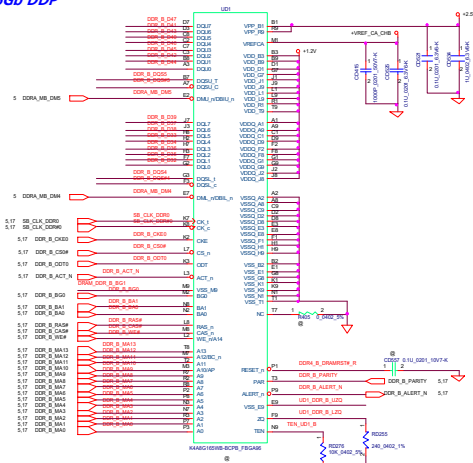
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Issued Date		Deciphered Date		SWITCH			
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				Custom	Drift AMD	0.1	
				Date:	Friday, March 01, 2019	Sheet	14 of 67



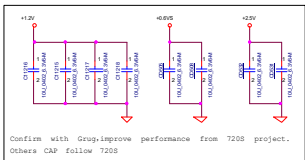
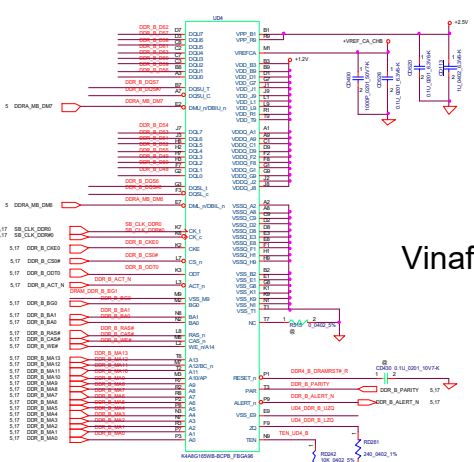
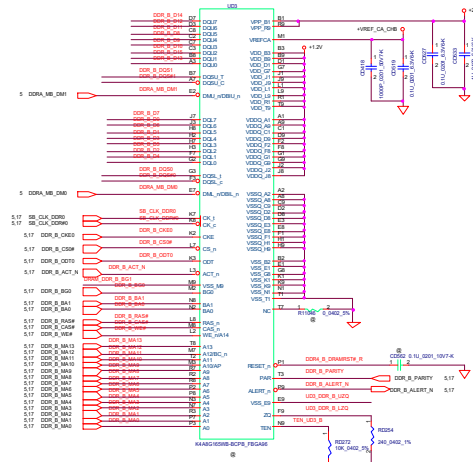


8Gb SDP
16Gb DDP

8Gb SDP
16Gb DDP



RD249 RD265 RD239 RD280 will install different value base on SDP or DDP.control by Virtual symbol



Confirm with Grug, improve performance from 720S project
Others CAP follow 720S

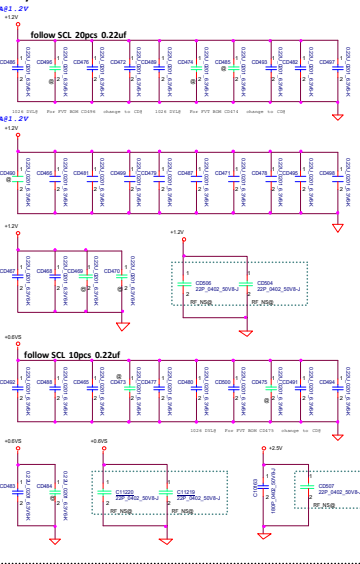
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Table 32. DDR4 DRAM Down Per-Channel Decoupling Capacitance

Capacitor		Configuration	VIDEO_MEM_03-V06	VIDEO_MEM_03-VTT	VTT-V06	VREF_CA-VIDEO_MEM_03
Value	Package/Size/Manufacturer					
0.1 μ F	0402 XTR	S0x16	-	-	-	4
0.22 μ F	0402 XTR	S0x16	18	5	5	-

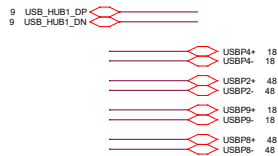


Layout Note: Place near DRA



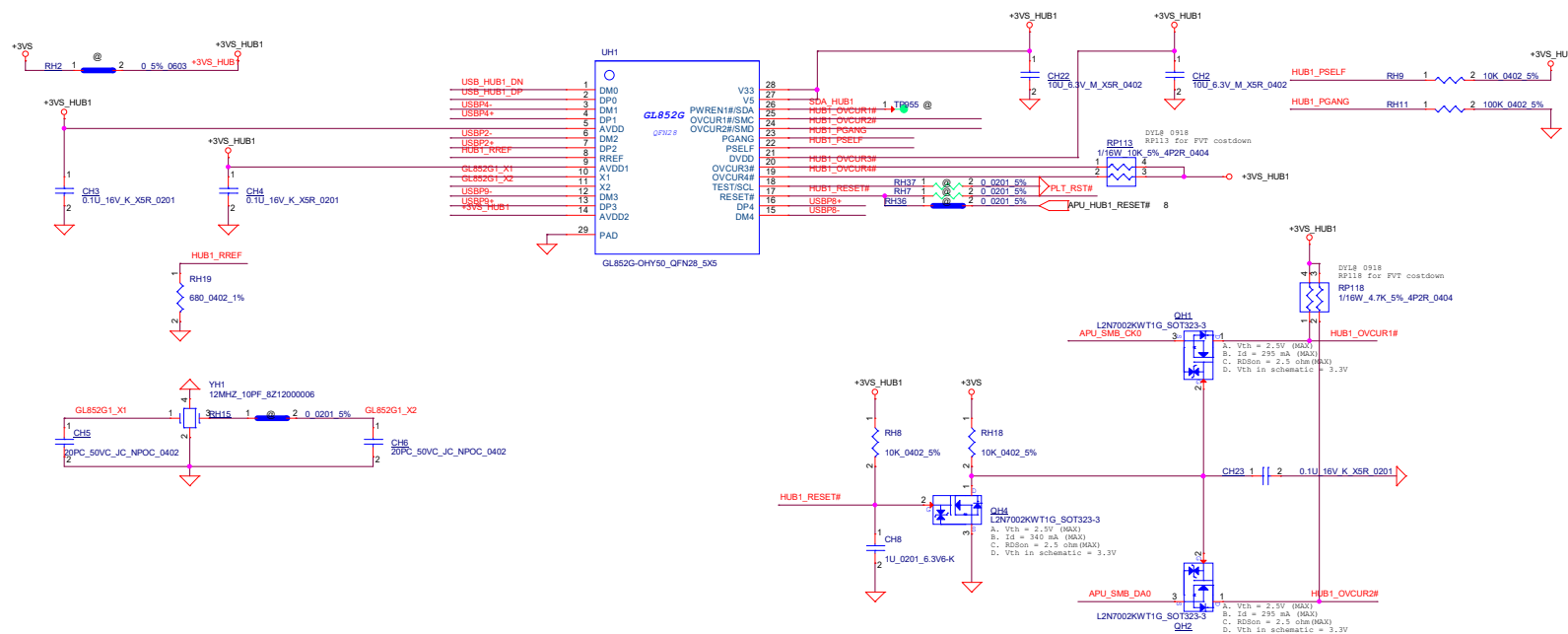
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<p align="center">Drift And</p>													
<p align="right">Drawing Number: D0R4-001 Date: 08/10/2015 Author: [Redacted] Checker: [Redacted]</p>													

USB Hub1

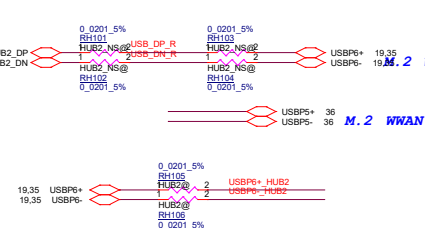


PORT1	IR Camera
PORT2	M.2 WLAN BT
PORT3	Touch Panel
PORT4	2D Camera

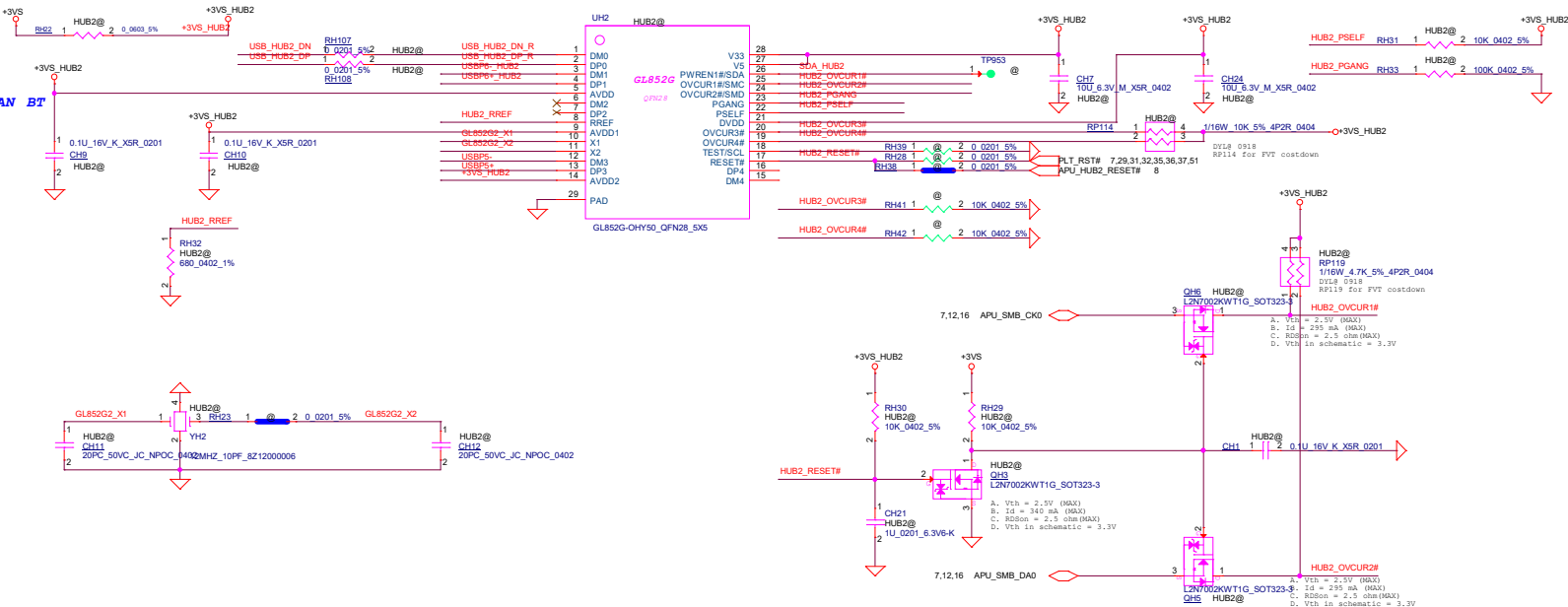
Camera
Smart Cart
Touch Panel
Fingerprint

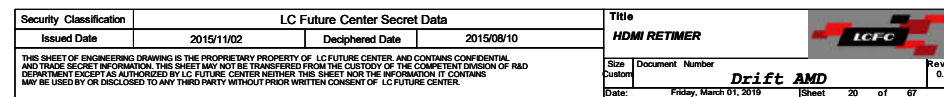


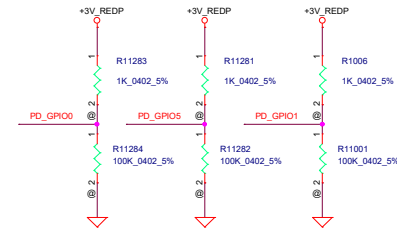
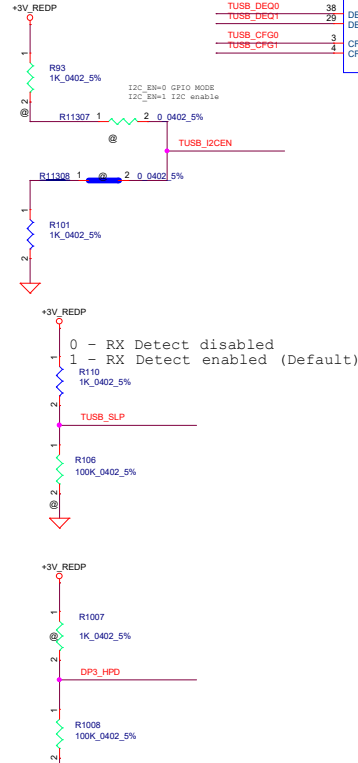
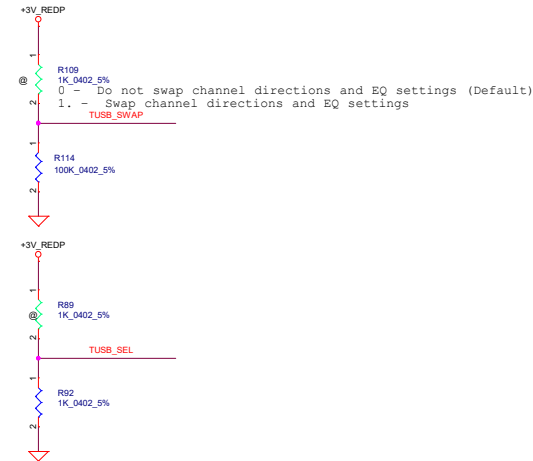
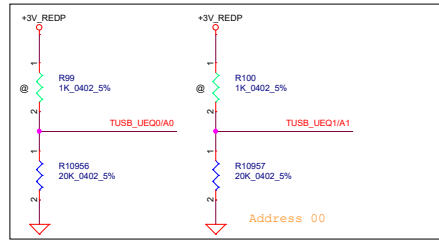
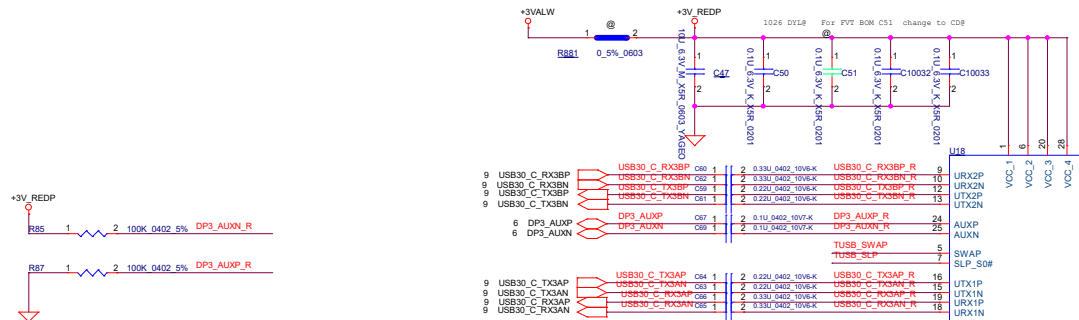
USB Hub2



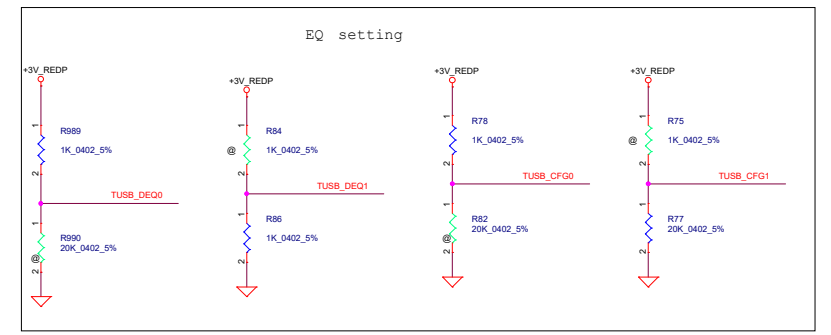
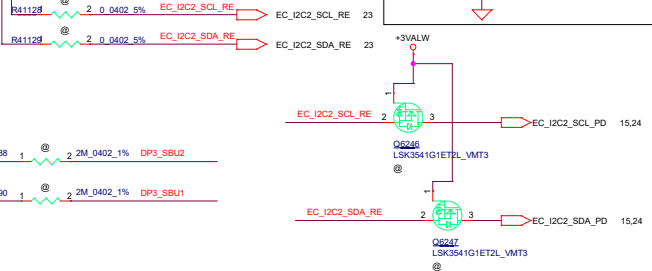
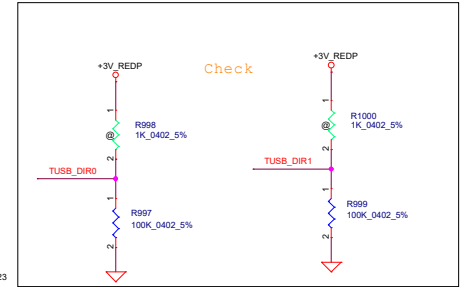
PORT1	Finger print
PORT3	M.2 WWAN
PORT4	smart card







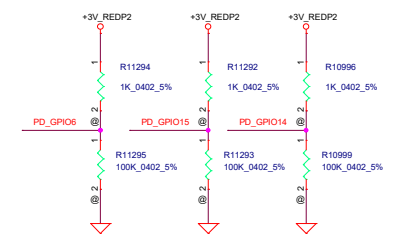
L = DisplayPort Disabled.
H = DisplayPort Enabled.
When I2C_EN = 0, this pin is not used by device.



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0 = 3.3-V configuration I/O voltage, 3.3-V I2C interface (Default)
1 = 1.8-V configuration I/O voltage, 1.8-V I2C interface

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Deciphered Date				2015/08/10				Size Document Number	
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Two circuit diagrams are shown side-by-side, illustrating a hardware configuration for a USB direction pin. The left diagram shows a correct configuration for `USB2_DIR0`, while the right diagram shows an incorrect configuration for `USB2_DIR1`.

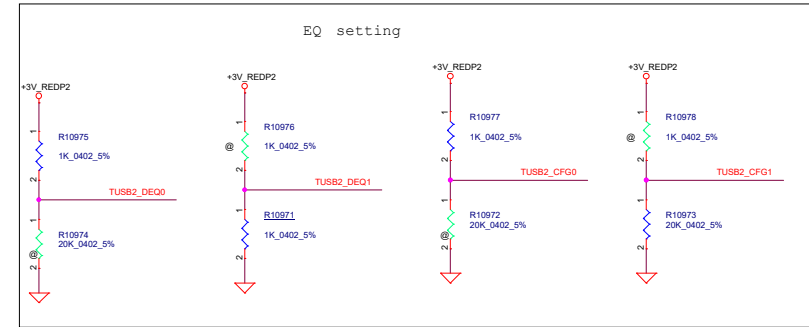
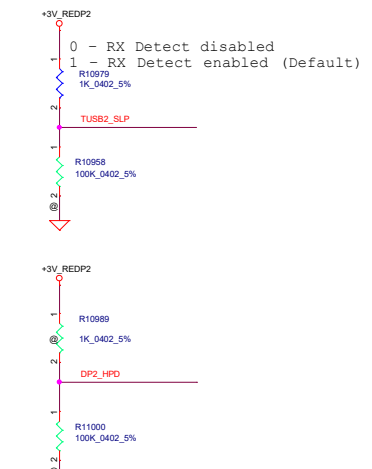
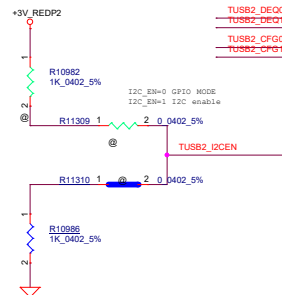
Left Diagram (Correct):



- Pin: `USB2_DIR0`
- Supply: `+3V_REDP2`
- Resistor: `R10995 1K_0402_5%` (pull-up)
- Resistor: `R10962 100K_0402_5%` (pull-down to GND)

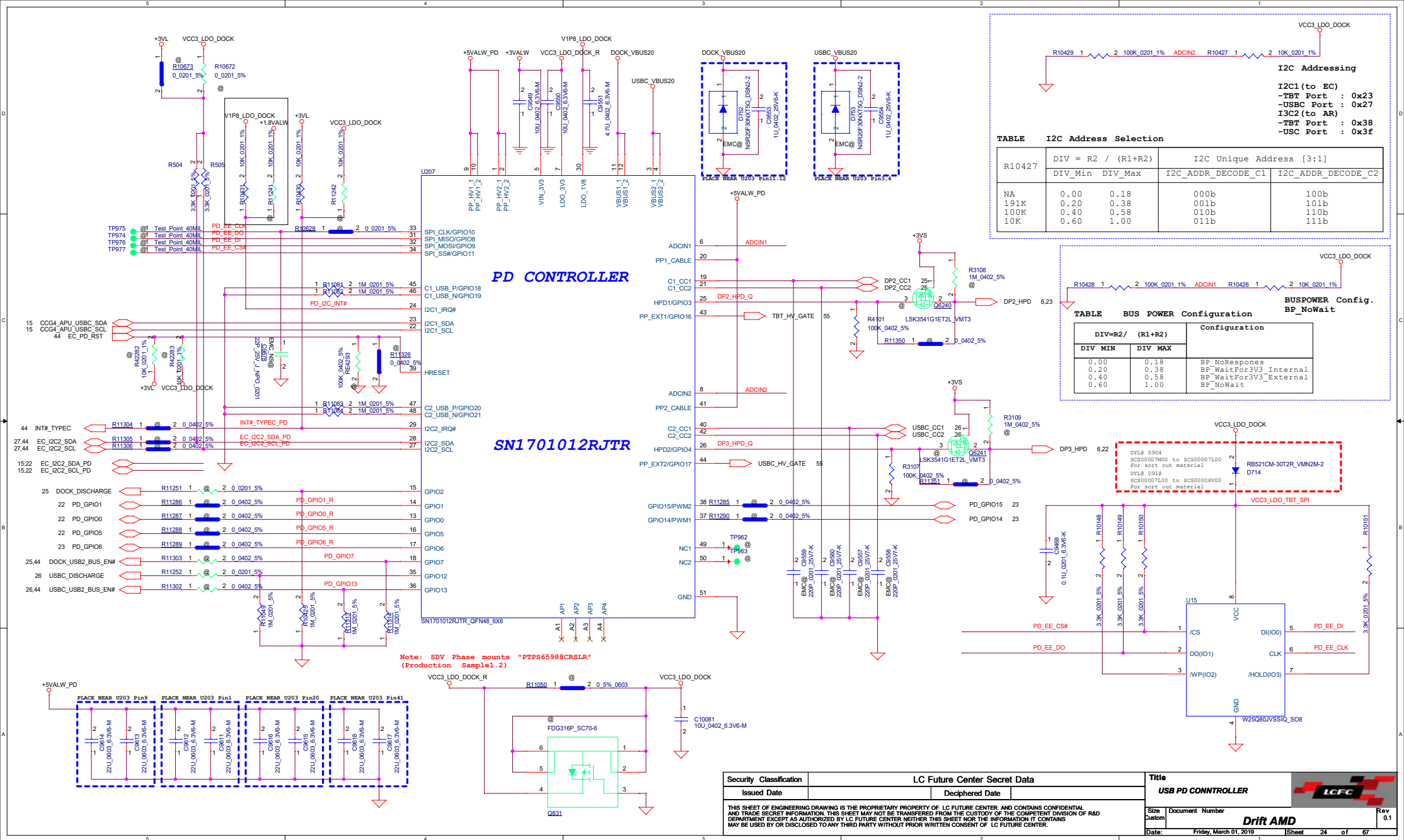
Right Diagram (Incorrect):

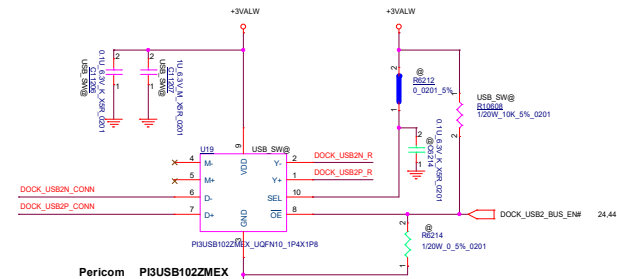
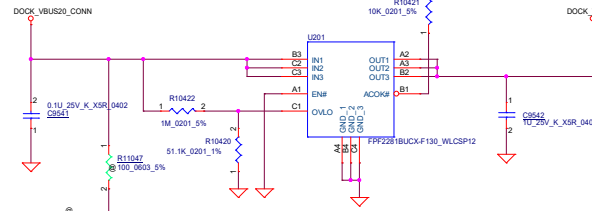
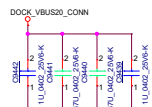
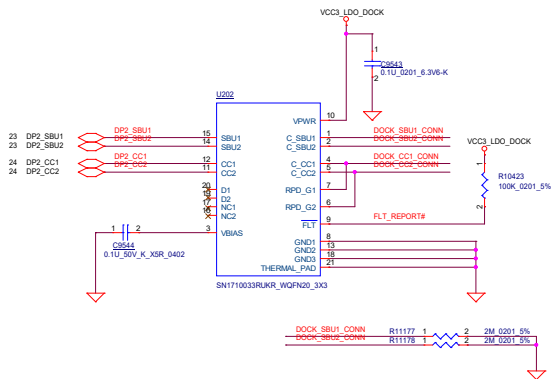
- Pin: `USB2_DIR1`
- Supply: `+3V_REDP2`
- Resistor: `R10994 1K_0402_5%` (pull-up)
- Resistor: `R10963 100K_0402_5%` (pull-down to GND)

A red 'X' is placed over the right diagram, and the word "Check" is written in orange above it, indicating that the `USB2_DIR1` configuration is incorrect.



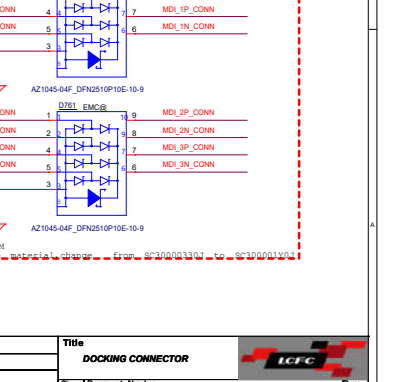
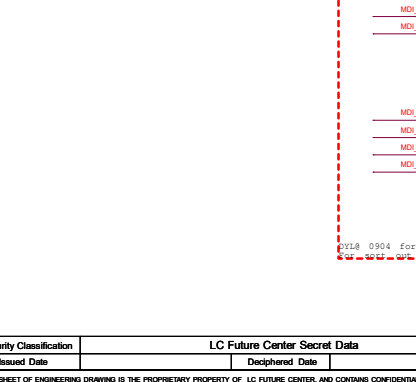
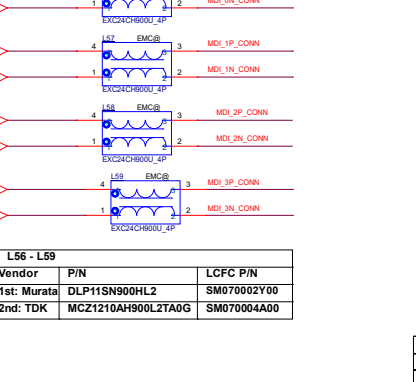
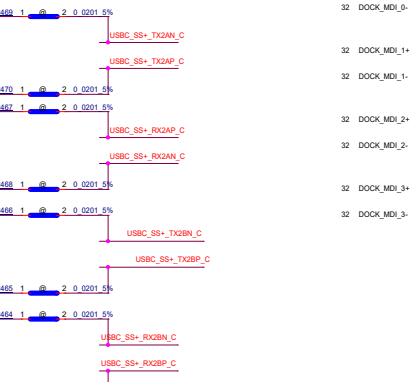
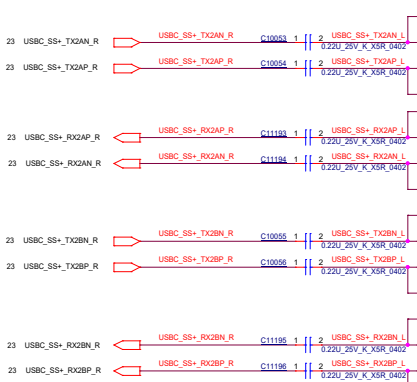
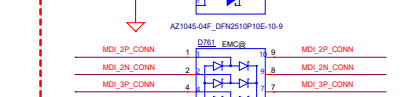
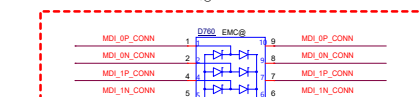
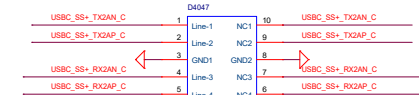
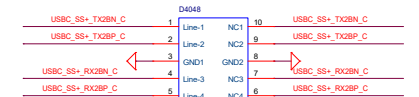
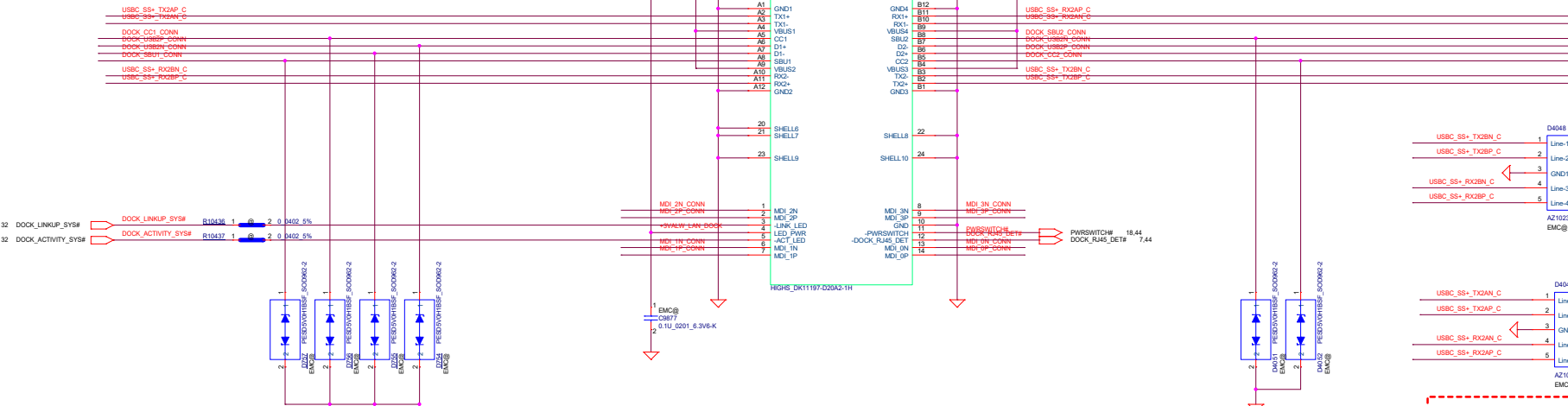
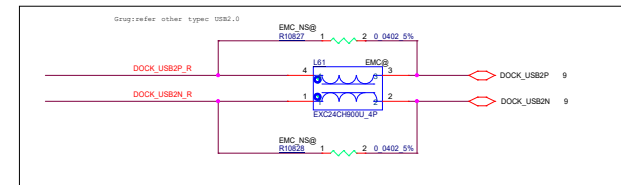
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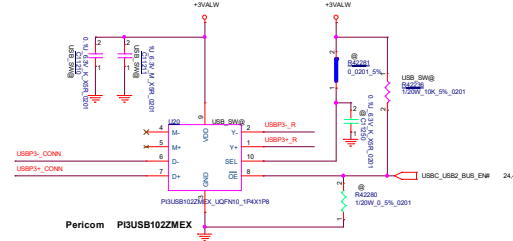
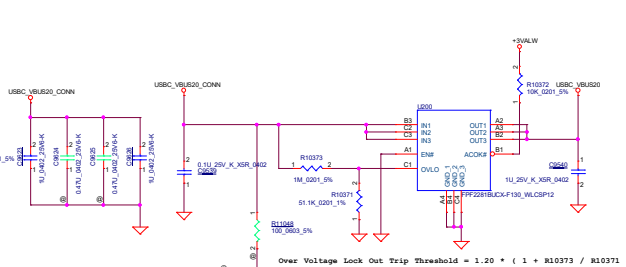
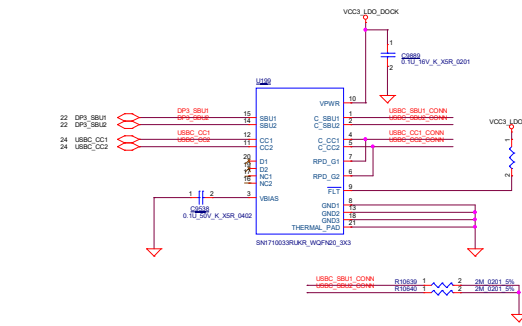


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BOURNS	MF-F5MF035X-2	SP040007700
AEM	PMS0603-035	SP040007B00

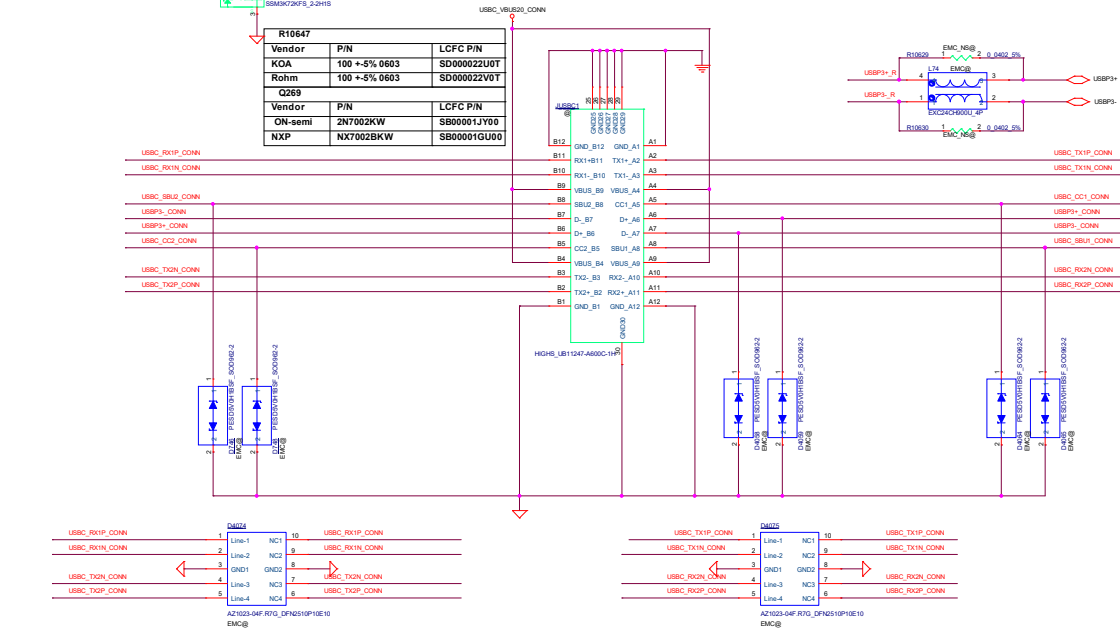
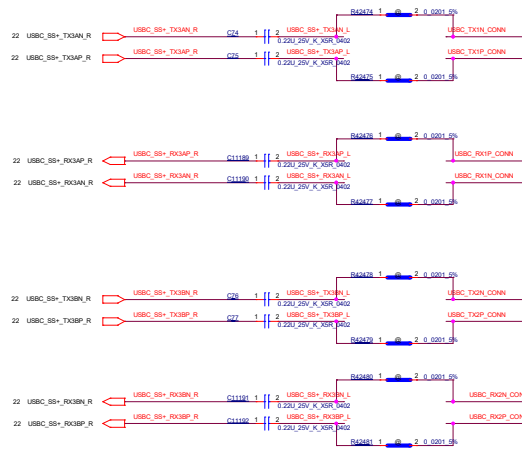
R10647		
Vendor	P/N	LCFC P/N
KOA	100 +5% 0603	SD000022U0T
Rohm	100 +5% 0603	SD000022V0T
Q269		
Vendor	P/N	LCFC P/N
ON-semi	2N7002KW	SB000001JY00
NXP	NX7002BKW	SB000001GU00








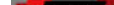
L56 - L59		
Vendor	P/N	LCFC P/N
1st: Murata	DLP11SN900HL2	SM070002Y00
2nd: TDK	MCZ1210AH900L2TA0G	SM070004A00



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Sheet	28 of 87						



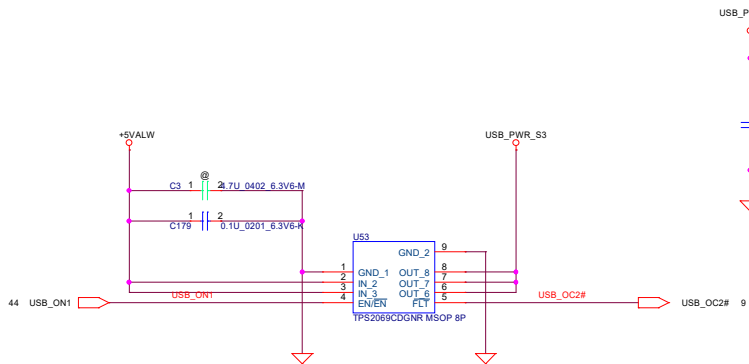
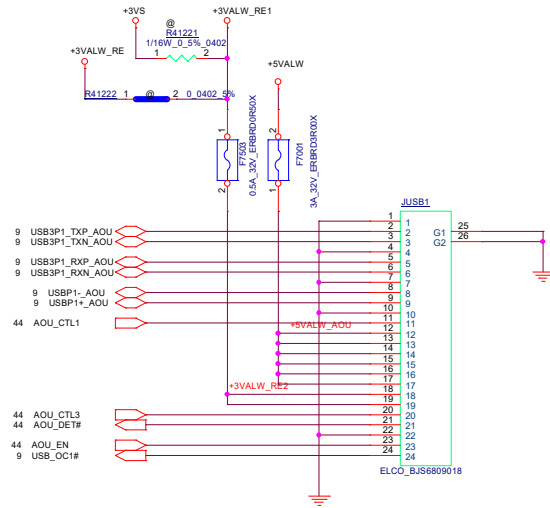
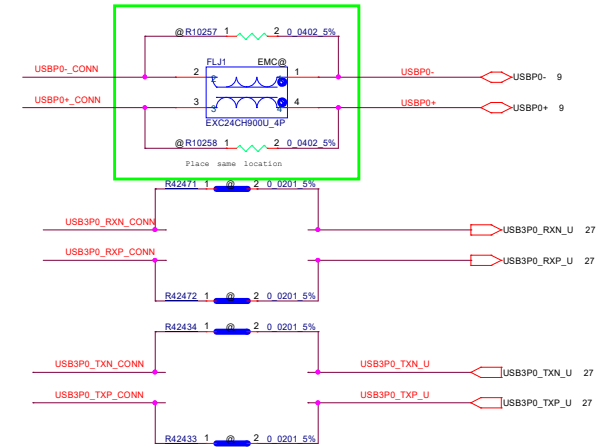
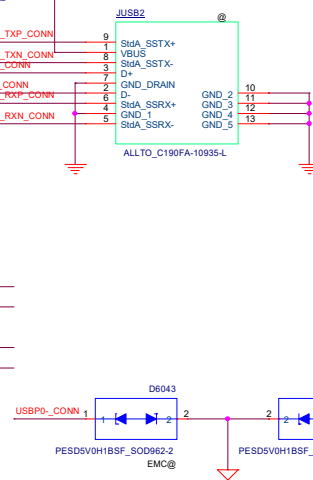
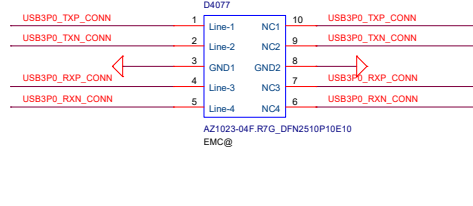
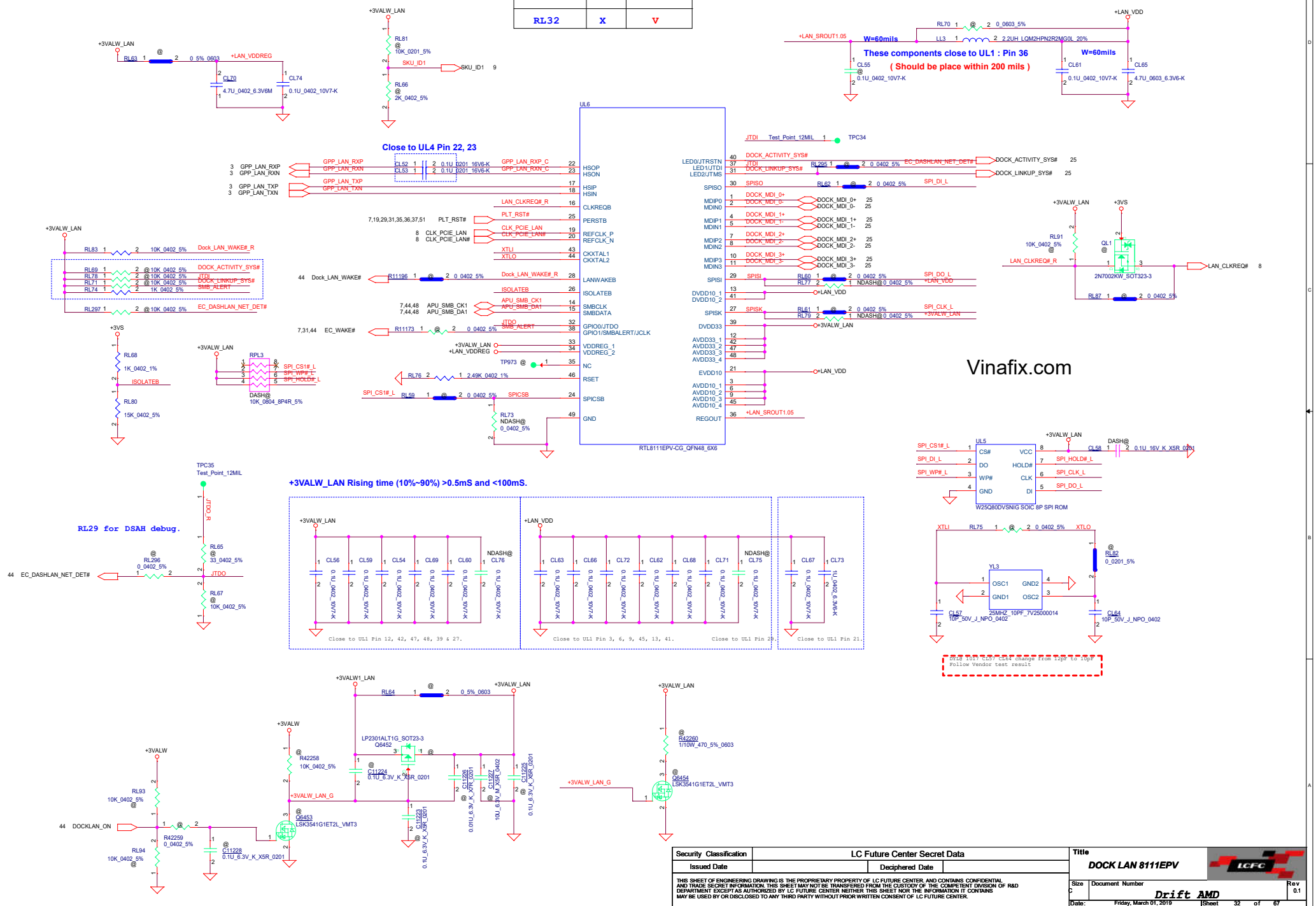



TABLE of USB3.0 Single (U53)		
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Rohm	G548A1F51U	SA00005RL00

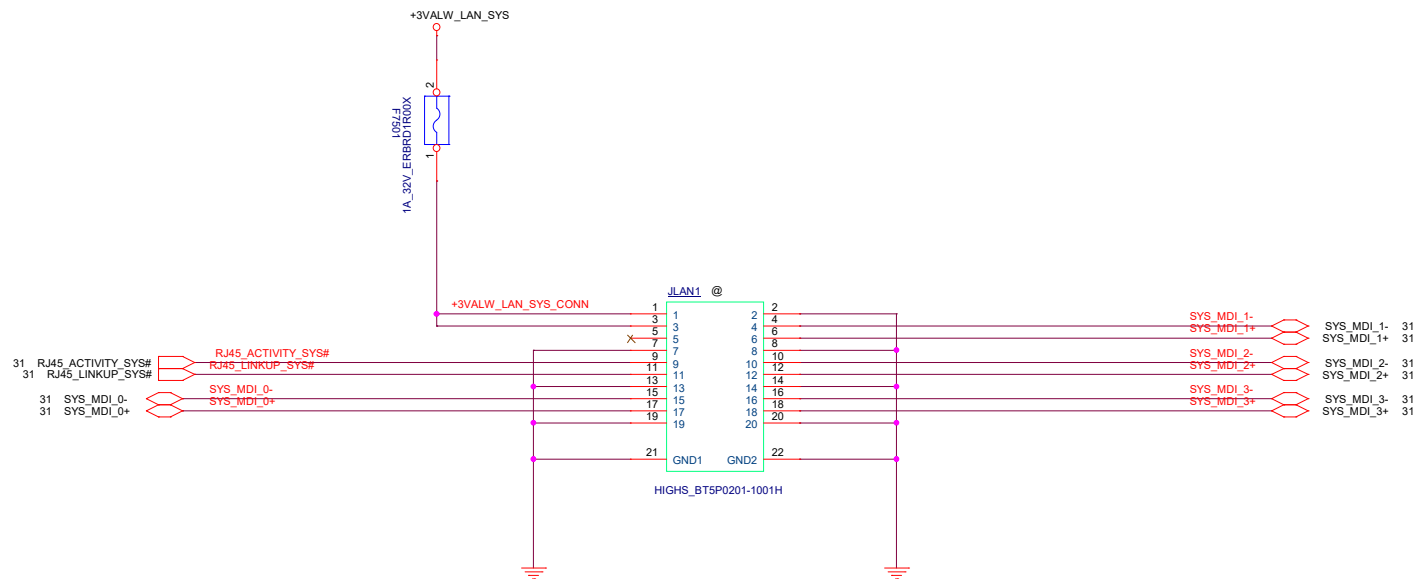


KevinH: USB3.1 GEN2 common choke follow TINY5, NEED CONFIRM

SKU_ID1	DASH	Non-DASH
RL31	V	X
RL32	X	V

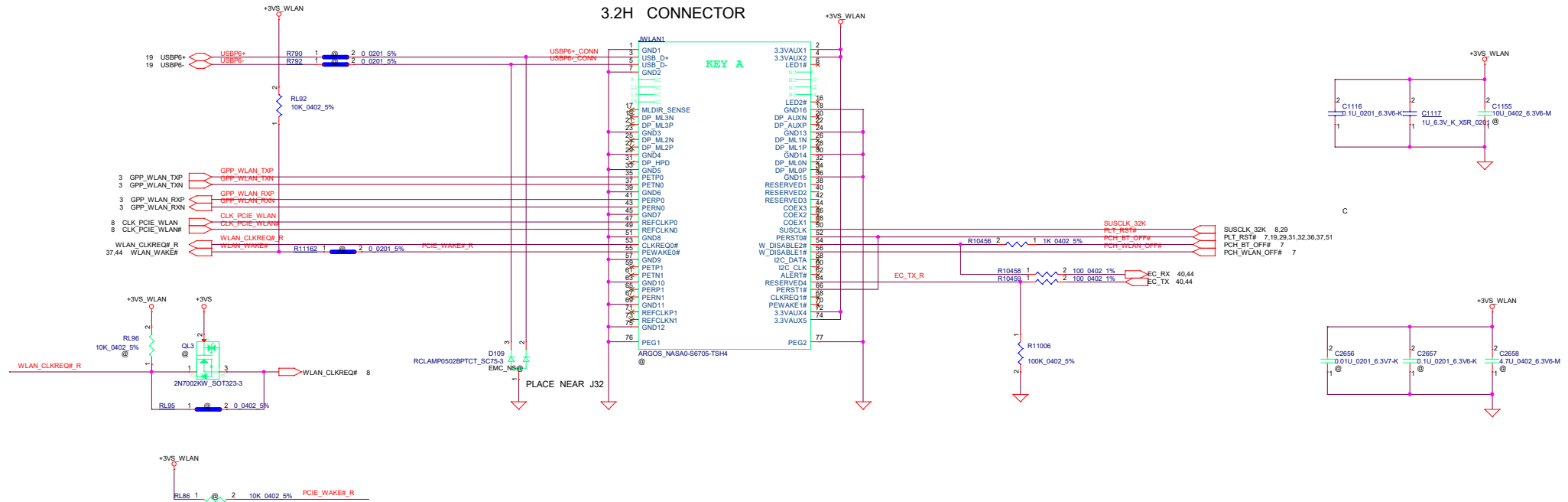



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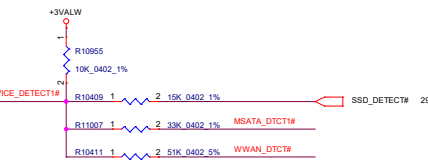
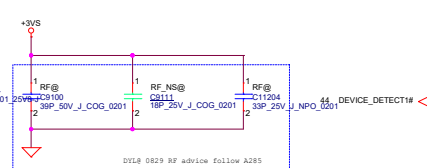
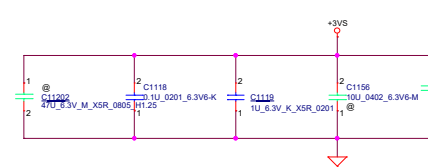
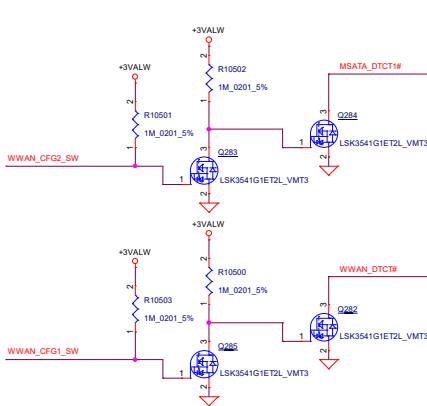


TYPE-A NGFF CARD FOR WLAN

3.2H CONNECTOR



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						Drift AMD			
						Date:	Friday, March 01, 2018	Sheet	35 of 67



Vcc	3.32V					
R10955	10K +/- 1%					
DEVICE_DETECT1#	1.992V	1.686V	1.533V	2.548V	2.215V	3.32V
SSD_DETECT#	V	V	V	X	X	X
MSATA_DTCT#	X	X	V	V	V	X
WWAN_DTCT#	X	X	V	X	V	X
Configuration	Only 22808SD	228042242 SSD	22805SD4WWAN	Only 2242SSD	Only WWAN	No Device

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TYPE-B NGFF CARD FOR WWAN 3.2H CONNECTOR

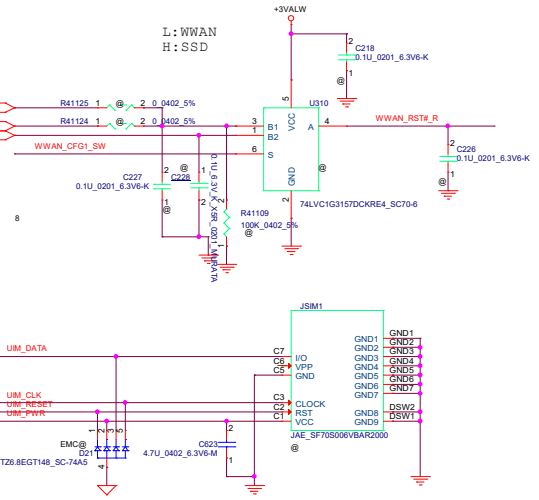
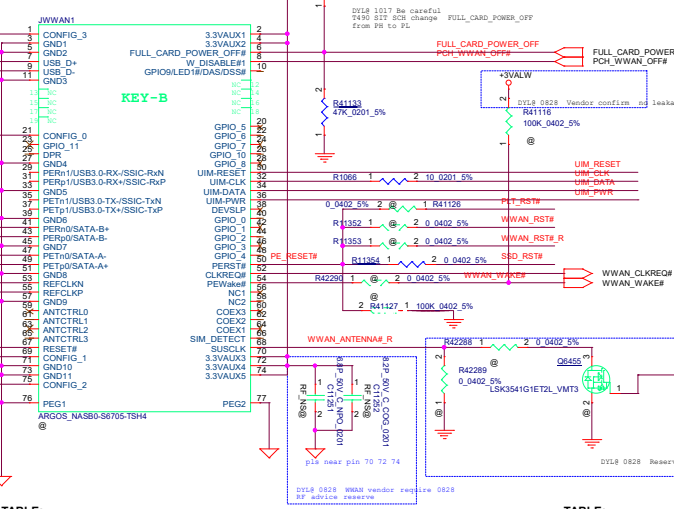
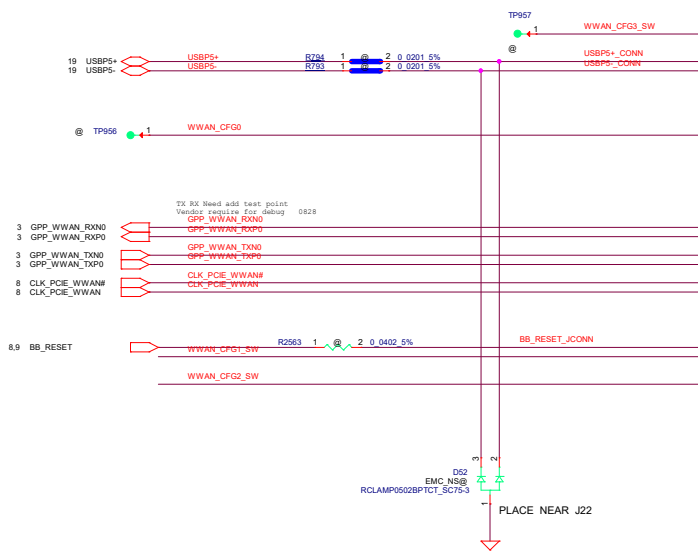
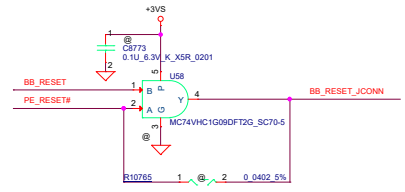
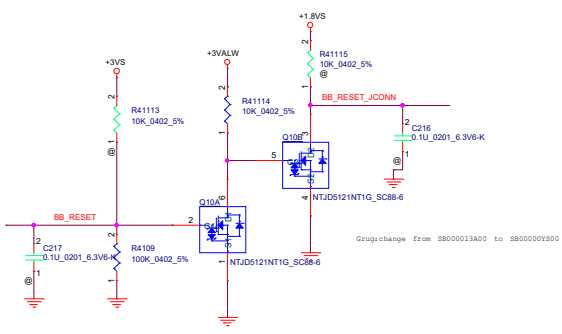


TABLE:

	Module Configuration Decodes	
CONFIG_0 (Pin 21)	CONFIG_3 (Pin 1)	CONFIG_2 (Pin 75)
Fibcom L830-EB	GND	NC
Sierra EM7565	GND	NC
2242 PCIe SSD	GND	NC

TABLE:

State #	CONFIG_0 (Pin 21)	CONFIG_3 (Pin 1)	CONFIG_2 (Pin 75)	CONFIG_1 (Pin 69)	Module Type and Main Host Interface	Port Configuration
0	GND	GND	GND	GND	SSD - SATA	N/A
1	GND	GND	GND	NC	SSD - PCIe	N/A
2	GND	GND	NC	GND	WWAN - PCIe	0
3	GND	GND	NC	NC	WWAN - PCIe	1
4	GND	NC	GND	GND	WWAN - USB 3.0	0
5	GND	NC	GND	NC	WWAN - USB 3.0	1
6	GND	NC	NC	GND	WWAN - USB 3.0	2
7	GND	NC	NC	NC	WWAN - USB 3.0	3
8	NC	GND	GND	GND	WWAN - SSIC	0
9	NC	GND	GND	NC	WWAN - SSIC	1
10	NC	GND	NC	GND	WWAN - SSIC	2
11	NC	GND	NC	NC	WWAN - SSIC	3
12	NC	NC	GND	GND	WWAN - PCIe	2
13	NC	NC	GND	NC	WWAN - PCIe	3
14	NC	NC	NC	GND	RFU	N/A
15	NC	NC	NC	NC	No Module Present	N/A



TABLE

Dock support		
	SUPPORT	NON-SUPPORT
R1235	NO ASM	NO ASM
R1236	ASM	NO ASM
R1237	ASM	NO ASM
R1238	ASM	NO ASM

↑
LOGIC

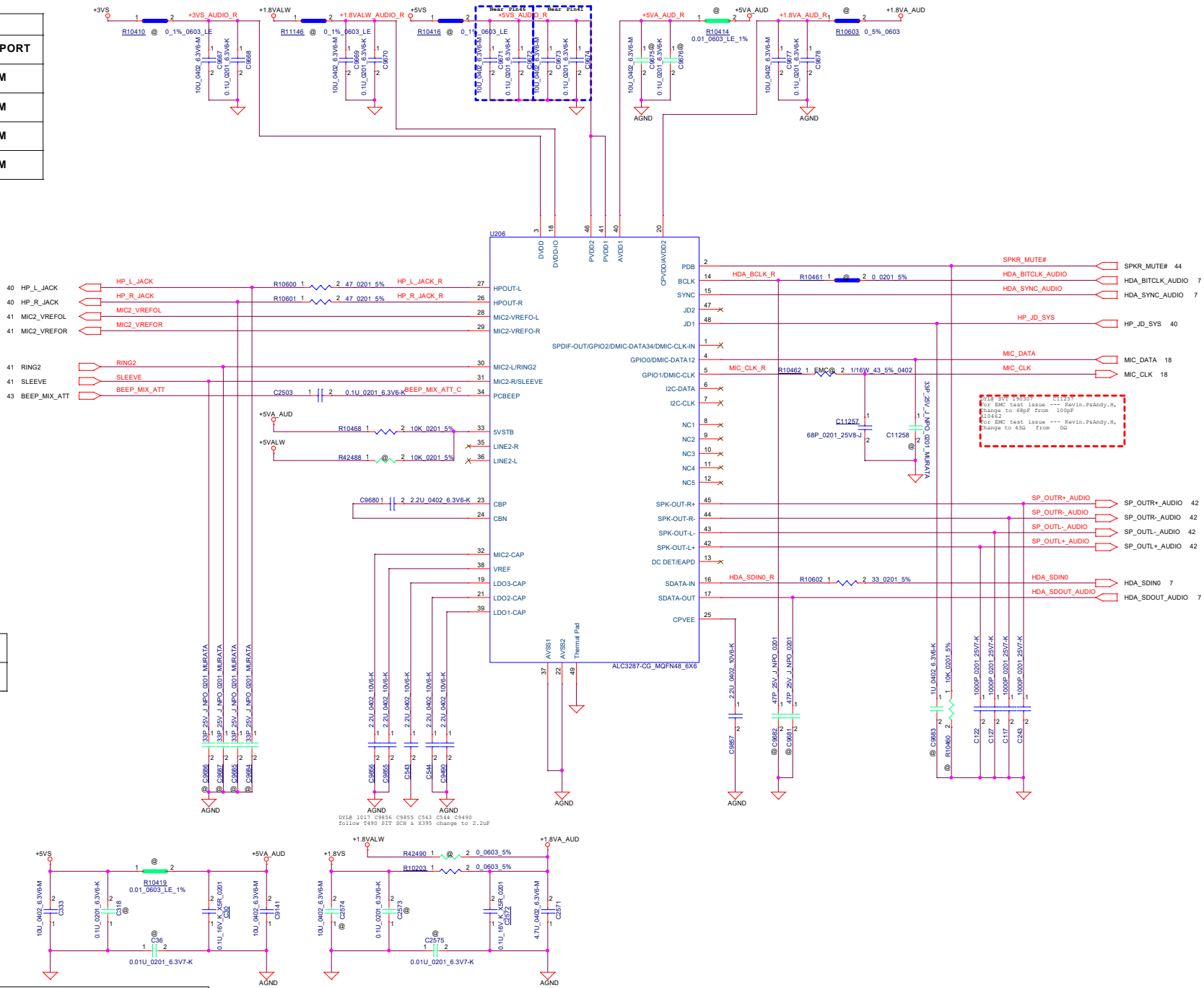
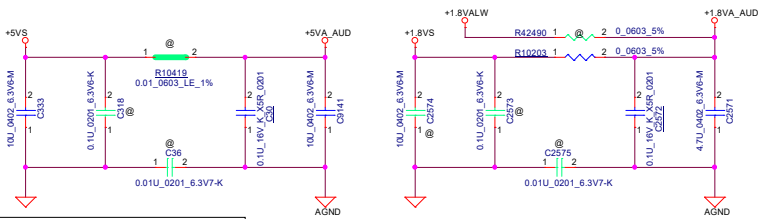


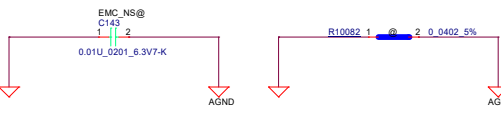
TABLE MIC HW ENABLE/DISABLE


	ENABLE	DISABLE
R961	ASM	NO ASM

↑
LOGIC

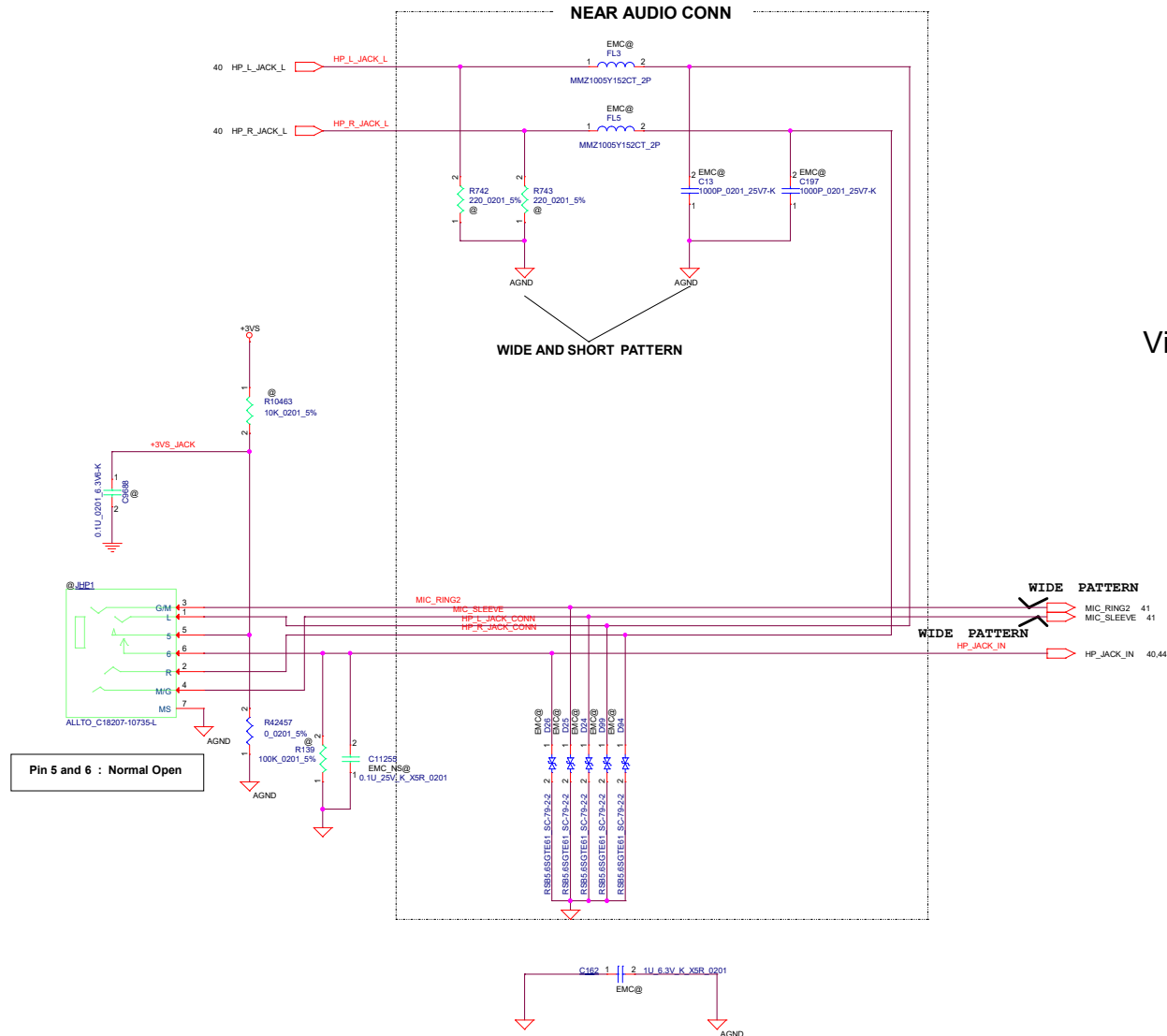



PLACE UNDER ALC3287

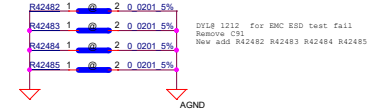
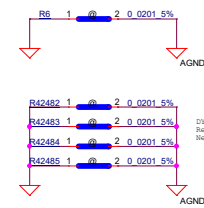
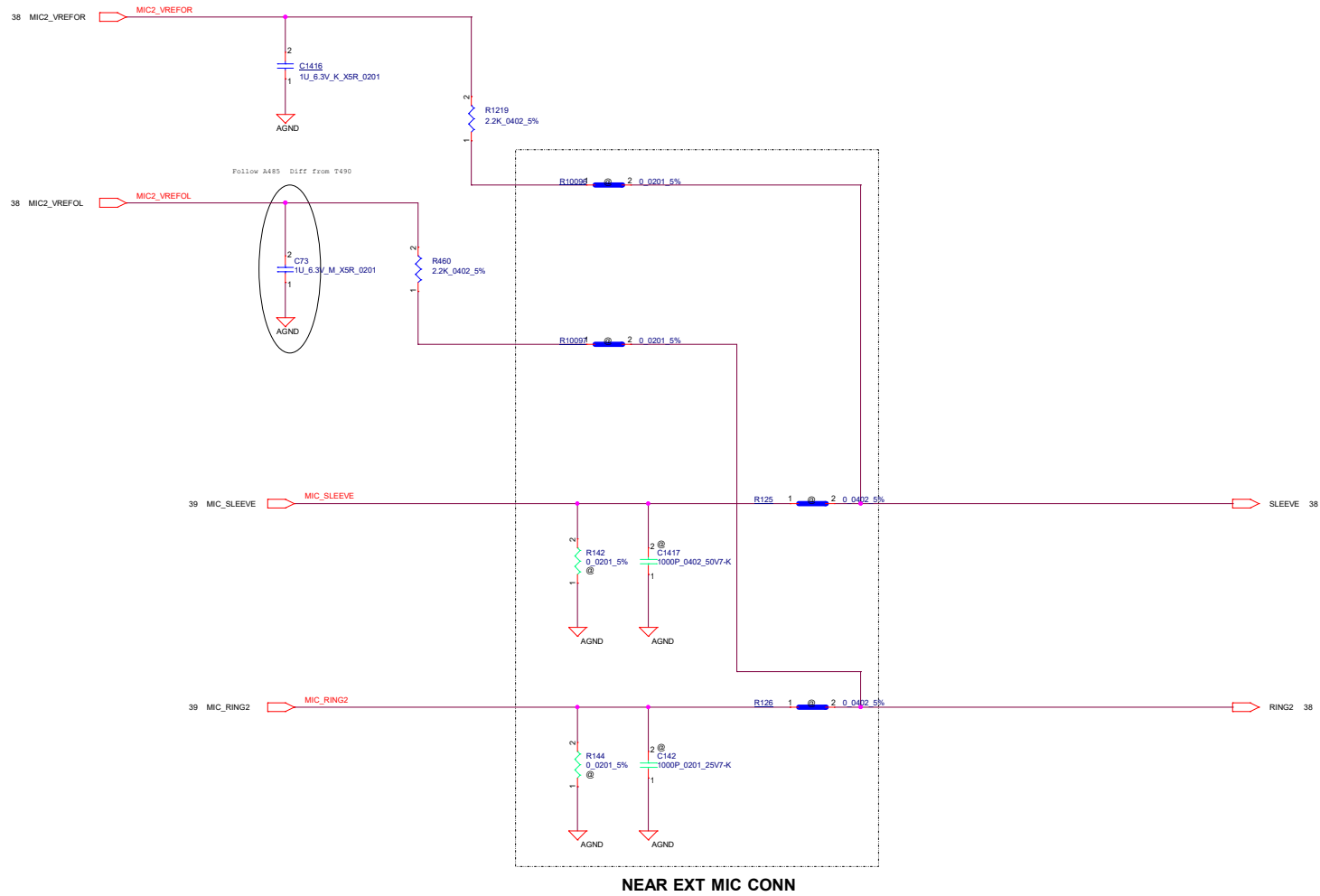


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Issued Date	2015/11/02	Deciphered Date	2015/8/10	AUDIO ALC3287-CG		
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Date:				Thursday, March 07, 2019	Sheet	38 of 67

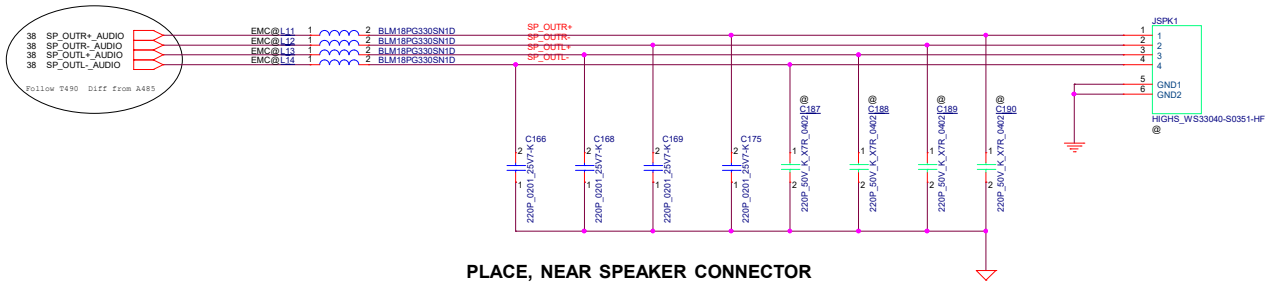
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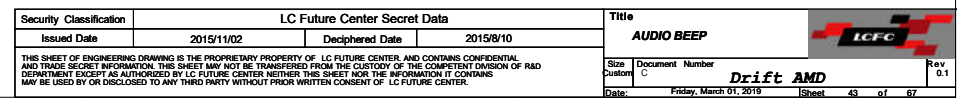
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C						0.1	
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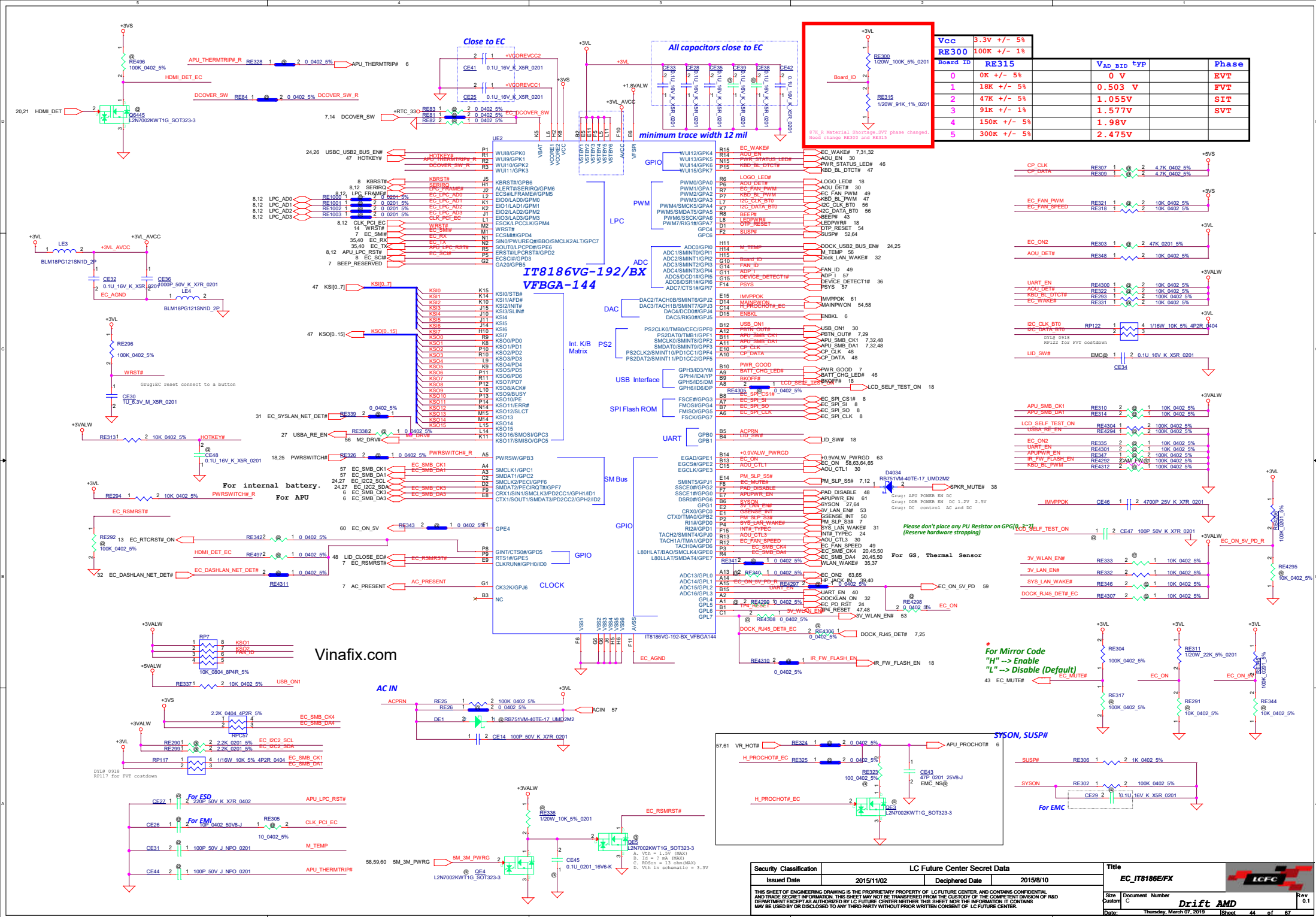


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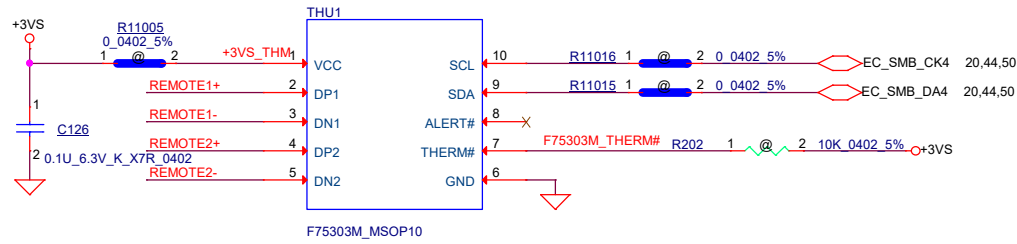




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Issued Date	2015/11/02	Deciphered Date	2015/8/10

Title			
EC_IT8186E/FX			
Size Custom	Document Number	Drift AMD	Rev 0.1
Date:	Thursday, March 07, 2019	Sheet	44 of 67

***Thermal Sensor
placed nearby SO-DIMM***

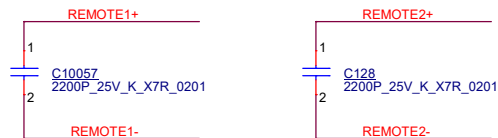


Address 1001_101xb

Internal pull up 1.2K to 1.5V

R for init i d t her md shut do wnt e mp

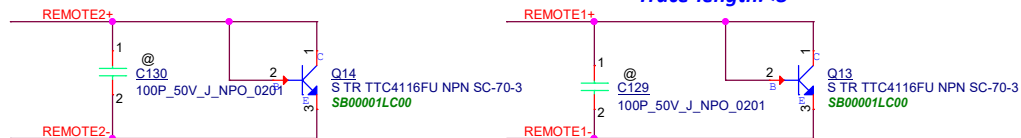
Close to THU1




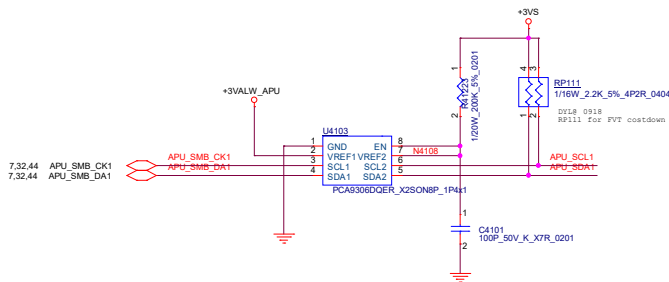
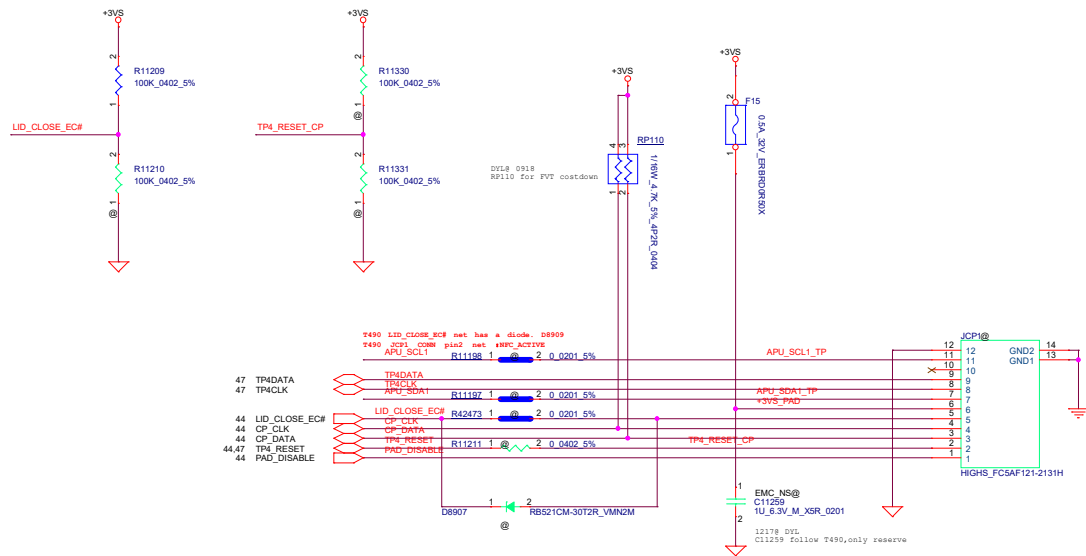
KevinH: Manual update PN to MMBT3904TT1G

REMOTE2+/-:
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Trace length:<8"

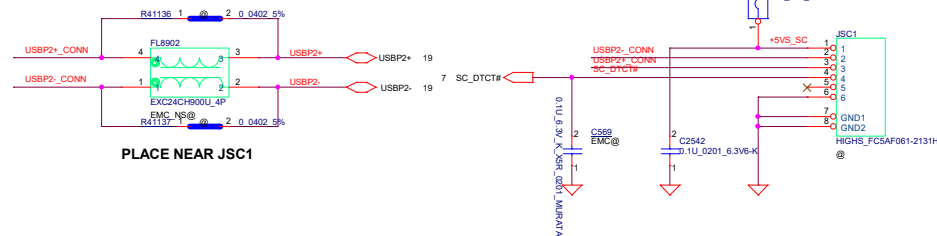
Close CHARGER



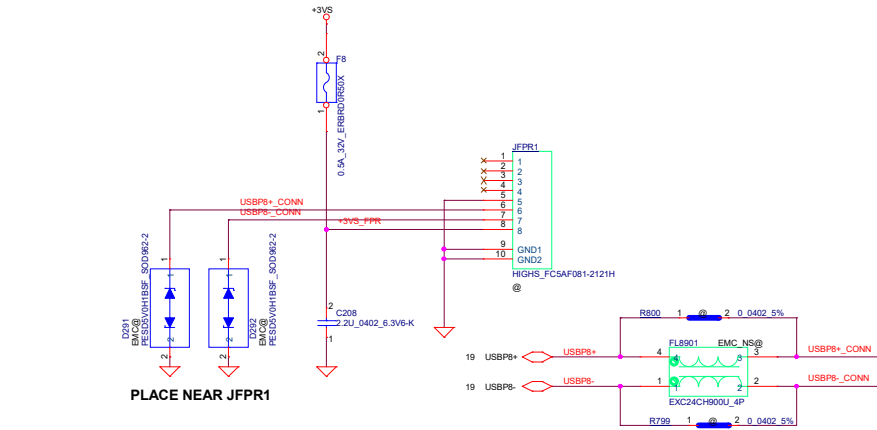
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Follow T490 I2C bus switch IC



PLACE NEAR JSC1



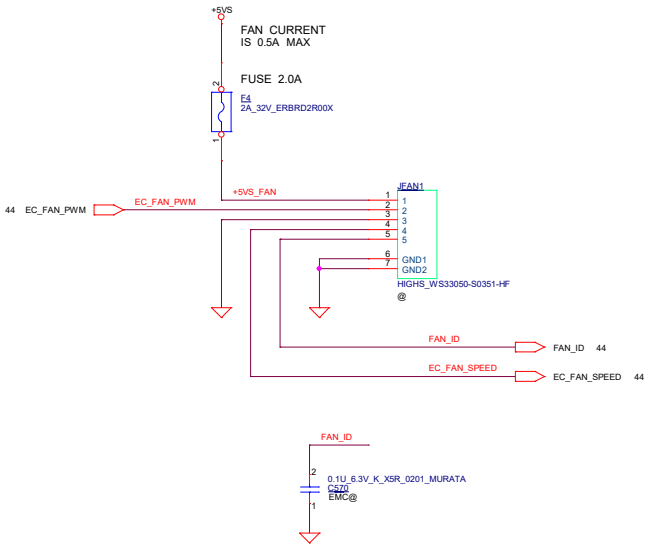
PLACE NEAR JFPR1


PLACE NEAR JFPR1

TABLE of U183/U184		
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TI	SN74LVC1G66DCK	SA00005BE0J
NXP	74LVC1G66GW	SA00007KS00
ONsemi	MC74VHC1G66DFT2G	SA00008JQ00

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C		Drift AMD			
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Size	Document Number					Rev	
C	Drift AMD					0.1	
Date:	Friday, March 01, 2018					Sheet	
						49 of 67	

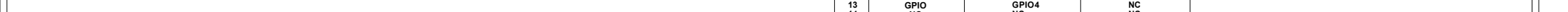
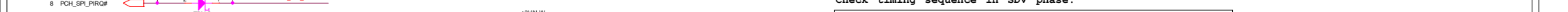


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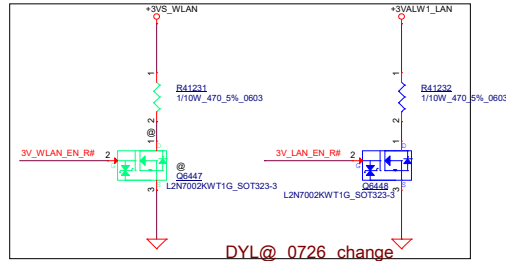
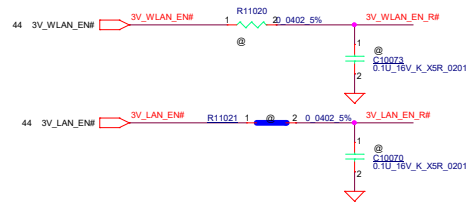
TABLE of TPM (U9801)			

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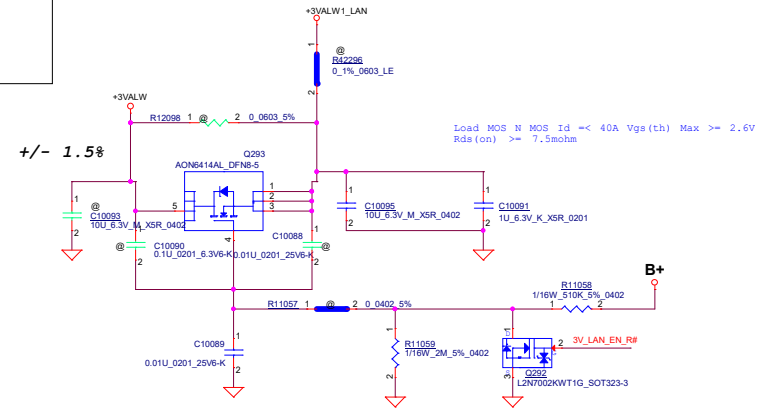
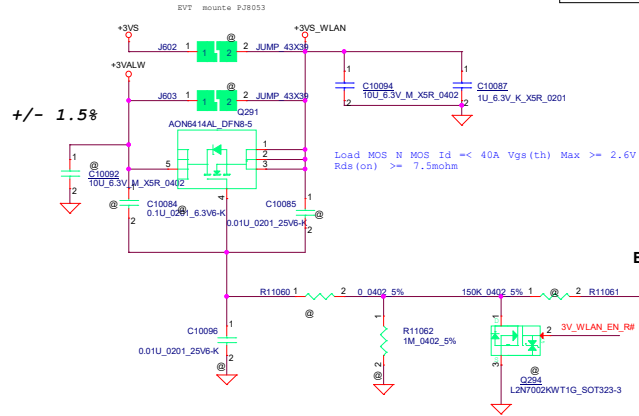
TPM⑧
RB751VM-40TE-17 UMD2M2


8 PCH_SPI_D0 R10318 1 2 0 0201.5% SPI_S1_R 21 MOSI/GPIO7

8 PCH_SPI_D1 MISO NC5 NC6 NC7 NC8 NC9 NC10 NC11 NC12 NC13 NC14 NC15 NC16 NC17 NC18 NC19 NC20 NC21 NC22 NC23 NC24 NC25 NC26 NC27 NC28 NC29 NC30 NC31 NC32 NC33 NC34 NC35 NC36 NC37 NC38 NC39 NC40 NC41 NC42 NC43 NC44 NC45 NC46 NC47 NC48 NC49 NC50 NC51 NC52 NC53 NC54 NC55 NC56 NC57 NC58 NC59 NC60 NC61 NC62 NC63 NC64 NC65



+3VS to +3VS_WLAN
J602 need to be mounted
 B+ 9V--13.6V

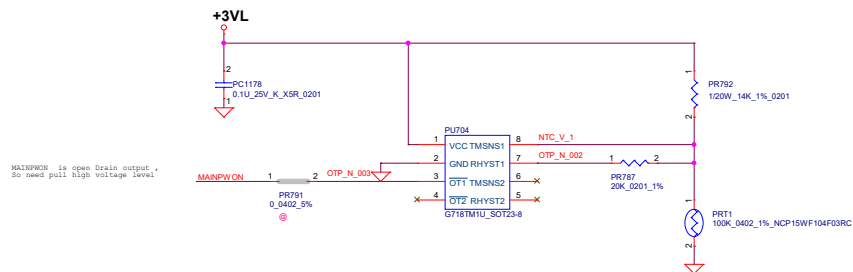



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				Date:	Friday, March 01, 2013	Sheet	53 of 67

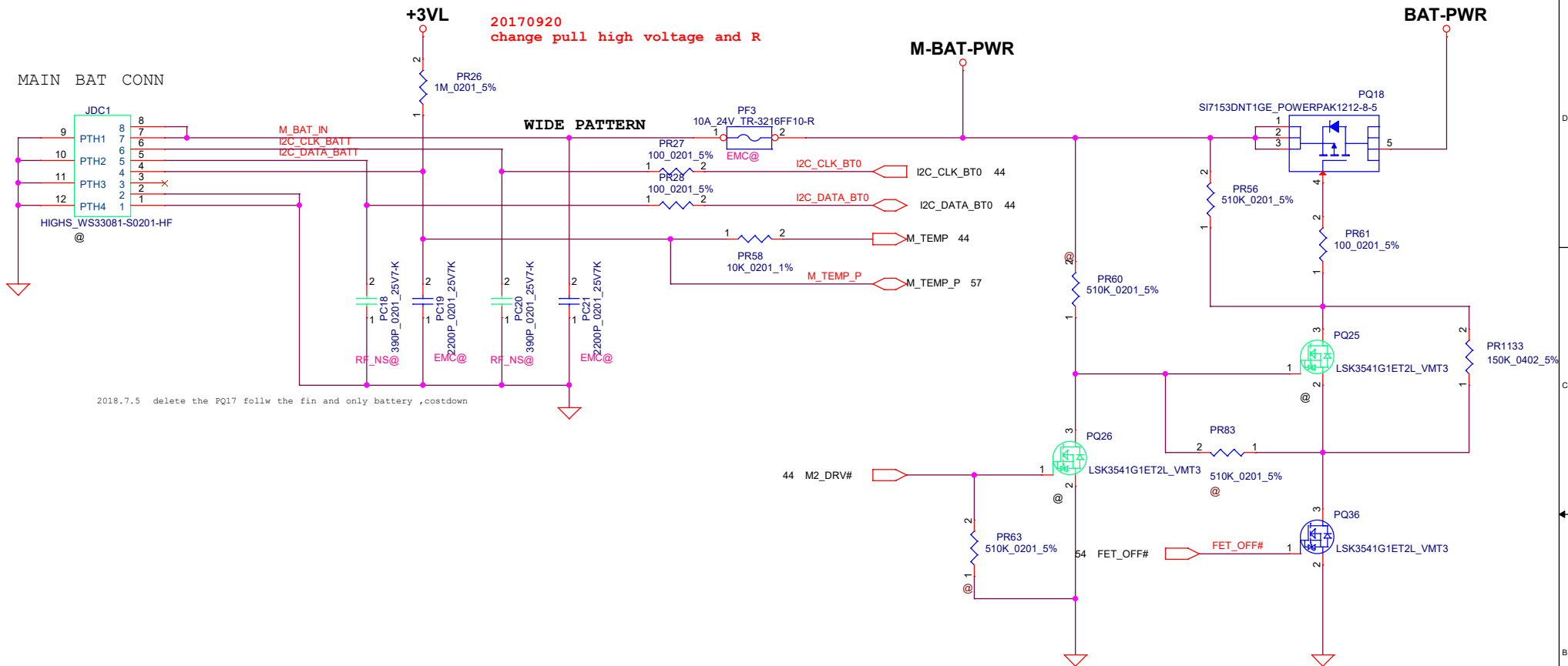
The schematic diagram illustrates the PMIC power management section, showing various power rails and their connections to the PMIC and external components. The diagram includes the following components and connections:


- Inputs:**
 - MAINPWON:** Open Drain output, connected to PR789 (2.0201_5%) and a pull-up resistor to 3.3V.
 - FET_OFF:** Input signal.
 - DCIN1:** DCIN1 FET (PQ17 FET) input.
 - DCIN2:** DCIN2 FET (PQ5 FET) input.
 - M-Battery IN:** M-Battery IN (PQ17 FET) input.
 - Charger:** Charger (PQ101 FET) input.
 - VDDC-VDD:** VDDC-VDD (PQ630 FET) input.
- PMIC and External Components:**
 - PMIC:** PMIC179, PMIC178, PMIC177, PMIC176, PMIC175, PMIC174, PMIC173, PMIC172, PMIC171, PMIC170, PMIC169, PMIC168, PMIC167, PMIC166, PMIC165, PMIC164, PMIC163, PMIC162, PMIC161, PMIC160, PMIC159, PMIC158, PMIC157, PMIC156, PMIC155, PMIC154, PMIC153, PMIC152, PMIC151, PMIC150, PMIC149, PMIC148, PMIC147, PMIC146, PMIC145, PMIC144, PMIC143, PMIC142, PMIC141, PMIC140, PMIC139, PMIC138, PMIC137, PMIC136, PMIC135, PMIC134, PMIC133, PMIC132, PMIC131, PMIC130, PMIC129, PMIC128, PMIC127, PMIC126, PMIC125, PMIC124, PMIC123, PMIC122, PMIC121, PMIC120, PMIC119, PMIC118, PMIC117, PMIC116, PMIC115, PMIC114, PMIC113, PMIC112, PMIC111, PMIC110, PMIC109, PMIC108, PMIC107, PMIC106, PMIC105, PMIC104, PMIC103, PMIC102, PMIC101, PMIC100, PMIC99, PMIC98, PMIC97, PMIC96, PMIC95, PMIC94, PMIC93, PMIC92, PMIC91, PMIC90, PMIC89, PMIC88, PMIC87, PMIC86, PMIC85, PMIC84, PMIC83, PMIC82, PMIC81, PMIC80, PMIC79, PMIC78, PMIC77, PMIC76, PMIC75, PMIC74, PMIC73, PMIC72, PMIC71, PMIC70, PMIC69, PMIC68, PMIC67, PMIC66, PMIC65, PMIC64, PMIC63, PMIC62, PMIC61, PMIC60, PMIC59, PMIC58, PMIC57, PMIC56, PMIC55, PMIC54, PMIC53, PMIC52, PMIC51, PMIC50, PMIC49, PMIC48, PMIC47, PMIC46, PMIC45, PMIC44, PMIC43, PMIC42, PMIC41, PMIC40, PMIC39, PMIC38, PMIC37, PMIC36, PMIC35, PMIC34, PMIC33, PMIC32, PMIC31, PMIC30, PMIC29, PMIC28, PMIC27, PMIC26, PMIC25, PMIC24, PMIC23, PMIC22, PMIC21, PMIC20, PMIC19, PMIC18, PMIC17, PMIC16, PMIC15, PMIC14, PMIC13, PMIC12, PMIC11, PMIC10, PMIC9, PMIC8, PMIC7, PMIC6, PMIC5, PMIC4, PMIC3, PMIC2, PMIC1, PMIC0.
 - Resistors:** PR789 (2.0201_5%), PR793 (100K_0201_1%), PR788 (10K_0201_1%), PR780 (750K_0201_5%), PR790 (10K_0201_1%), PR791 (10K_0201_1%), PR792 (10K_0201_1%), PR793 (100K_0201_1%), PR794 (10K_0201_1%), PR795 (10K_0201_1%), PR796 (10K_0201_1%), PR797 (10K_0201_1%), PR798 (10K_0201_1%), PR799 (10K_0201_1%), PR800 (10K_0201_1%), PR801 (10K_0201_1%), PR802 (10K_0201_1%), PR803 (10K_0201_1%), PR804 (10K_0201_1%), PR805 (10K_0201_1%), PR806 (10K_0201_1%), PR807 (10K_0201_1%), PR808 (10K_0201_1%), PR809 (10K_0201_1%), PR810 (10K_0201_1%), PR811 (10K_0201_1%), PR812 (10K_0201_1%), PR813 (10K_0201_1%), PR814 (10K_0201_1%), PR815 (10K_0201_1%), PR816 (10K_0201_1%), PR817 (10K_0201_1%), PR818 (10K_0201_1%), PR819 (10K_0201_1%), PR820 (10K_0201_1%), PR821 (10K_0201_1%), PR822 (10K_0201_1%), PR823 (10K_0201_1%), PR824 (10K_0201_1%), PR825 (10K_0201_1%), PR826 (10K_0201_1%), PR827 (10K_0201_1%), PR828 (10K_0201_1%), PR829 (10K_0201_1%), PR830 (10K_0201_1%), PR831 (10K_0201_1%), PR832 (10K_0201_1%), PR833 (10K_0201_1%), PR834 (10K_0201_1%), PR835 (10K_0201_1%), PR836 (10K_0201_1%), PR837 (10K_0201_1%), PR838 (10K_0201_1%), PR839 (10K_0201_1%), PR840 (10K_0201_1%), PR841 (10K_0201_1%), PR842 (10K_0201_1%), PR843 (10K_0201_1%), PR844 (10K_0201_1%), PR845 (10K_0201_1%), PR846 (10K_0201_1%), PR847 (10K_0201_1%), PR848 (10K_0201_1%), PR849 (10K_0201_1%), PR850 (10K_0201_1%), PR851 (10K_0201_1%), PR852 (10K_0201_1%), PR853 (10K_0201_1%), PR854 (10K_0201_1%), PR855 (10K_0201_1%), PR856 (10K_0201_1%), PR857 (10K_0201_1%), PR858 (10K_0201_1%), PR859 (10K_0201_1%), PR860 (10K_0201_1%), PR861 (10K_0201_1%), PR862 (10K_0201_1%), PR863 (10K_0201_1%), PR864 (10K_0201_1%), PR865 (10K_0201_1%), PR866 (10K_0201_1%), PR867 (10K_0201_1%), PR868 (10K_0201_1%), PR869 (10K_0201_1%), PR870 (10K_0201_1%), PR871 (10K_0201_1%), PR872 (10K_0201_1%), PR873 (10K_0201_1%), PR874 (10K_0201_1%), PR875 (10K_0201_1%), PR876 (10K_0201_1%), PR877 (10K_0201_1%), PR878 (10K_0201_1%), PR879 (10K_0201_1%), PR880 (10K_0201_1%), PR881 (10K_0201_1%), PR882 (10K_0201_1%), PR883 (10K_0201_1%), PR884 (10K_0201_1%), PR885 (10K_0201_1%), PR886 (10K_0201_1%), PR887 (10K_0201_1%), PR888 (10K_0201_1%), PR889 (10K_0201_1%), PR890 (10K_0201_1%), PR891 (10K_0201_1%), PR892 (10K_0201_1%), PR893 (10K_0201_1%), PR894 (10K_0201_1%), PR895 (10K_0201_1%), PR896 (10K_0201_1%), PR897 (10K_0201_1%), PR898 (10K_0201_1%), PR899 (10K_0201_1%), PR900 (10K_0201_1%), PR901 (10K_0201_1%), PR902 (10K_0201_1%), PR903 (10K_0201_1%), PR904 (10K_0201_1%), PR905 (10K_0201_1%), PR906 (10K_0201_1%), PR907 (10K_0201_1%), PR908 (10K_0201_1%), PR909 (10K_0201_1%), PR910 (10K_0201_1%), PR911 (10K_0201_1%), PR912 (10K_0201_1%), PR913 (10K_0201_1%), PR914 (10K_0201_1%), PR915 (10K_0201_1%), PR916 (10K_0201_1%), PR917 (10K_0201_1%), PR918 (10K_0201_1%), PR919 (10K_0201_1%), PR920 (10K_0201_1%), PR921 (10K_0201_1%), PR922 (10K_0201_1%), PR923 (10K_0201_1%), PR924 (10K_0201_1%), PR925 (10K_0201_1%), PR926 (10K_0201_1%), PR927 (10K_0201_1%), PR928 (10K_0201_1%), PR929 (10K_0201_1%), PR930 (10K_0201_1%), PR931 (10K_0201_1%), PR932 (10K_0201_1%), PR933 (10K_0201_1%), PR934 (10K_0201_1%), PR935 (10K_0201_1%), PR936 (10K_0201_1%), PR937 (10K_0201_1%), PR938 (10K_0201_1%), PR939 (10K_0201_1%), PR940 (10K_0201_1%), PR941 (10K_0201_1%), PR942 (10K_0201_1%), PR943 (10K_0201_1%), PR944 (10K_0201_1%), PR945 (10K_0201_1%), PR946 (10K_0201_1%), PR947 (10K_0201_1%), PR948 (10K_0201_1%), PR949 (10K_0201_1%), PR950 (10K_0201_1%), PR951 (10K_0201_1%), PR952 (10K_0201_1%), PR953 (10K_0201_1%), PR954 (10K_0201_1%), PR955 (10K_0201_1%), PR956 (10K_0201_1%), PR957 (10K_0201_1%), PR958 (10K_0201_1%), PR959 (10K_0201_1%), PR960 (10K_0201_

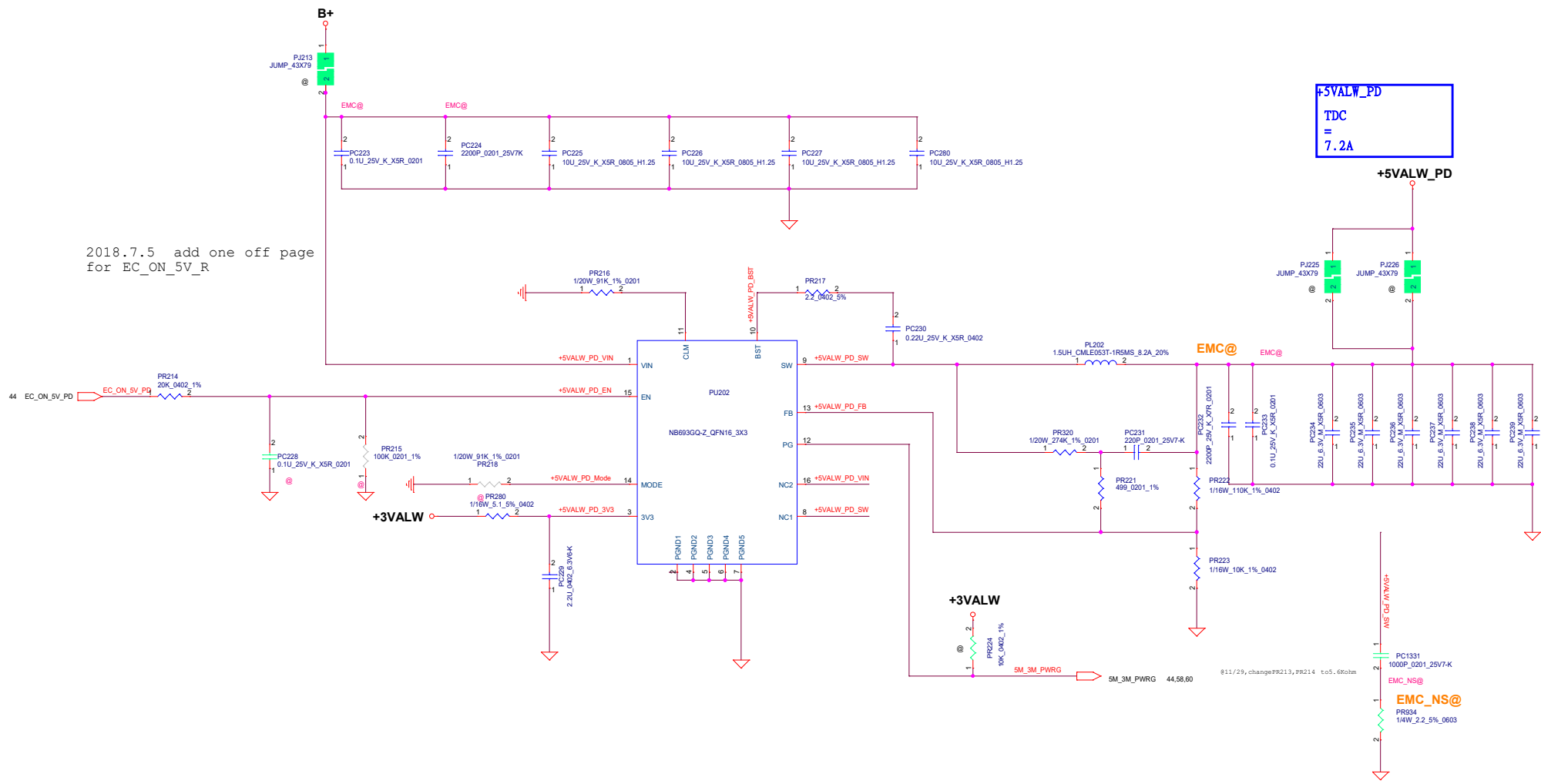
PRT7 under CPU botten side :
CPU thermal protection at 93 +3 degree C
Recovery at 56 +3 degree C

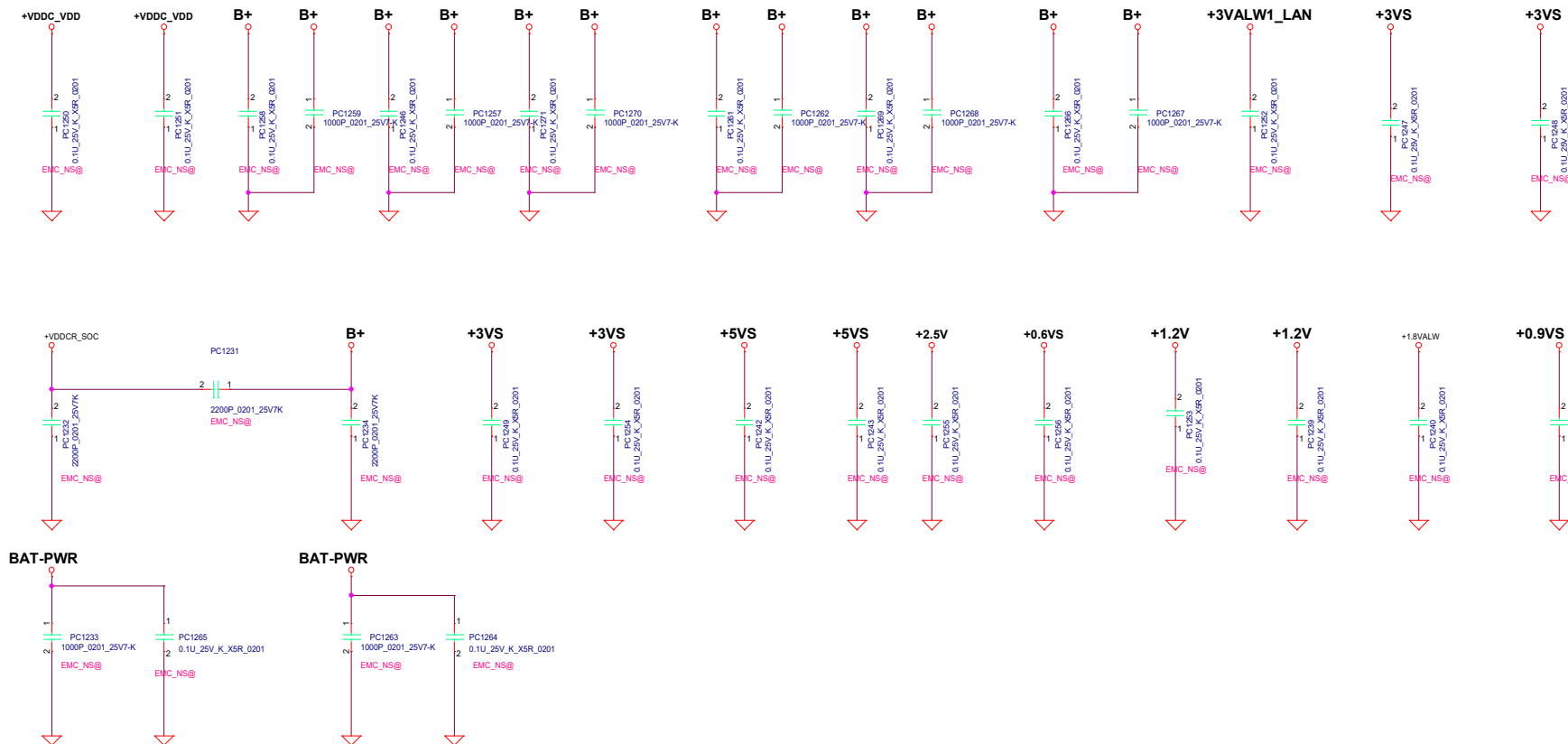
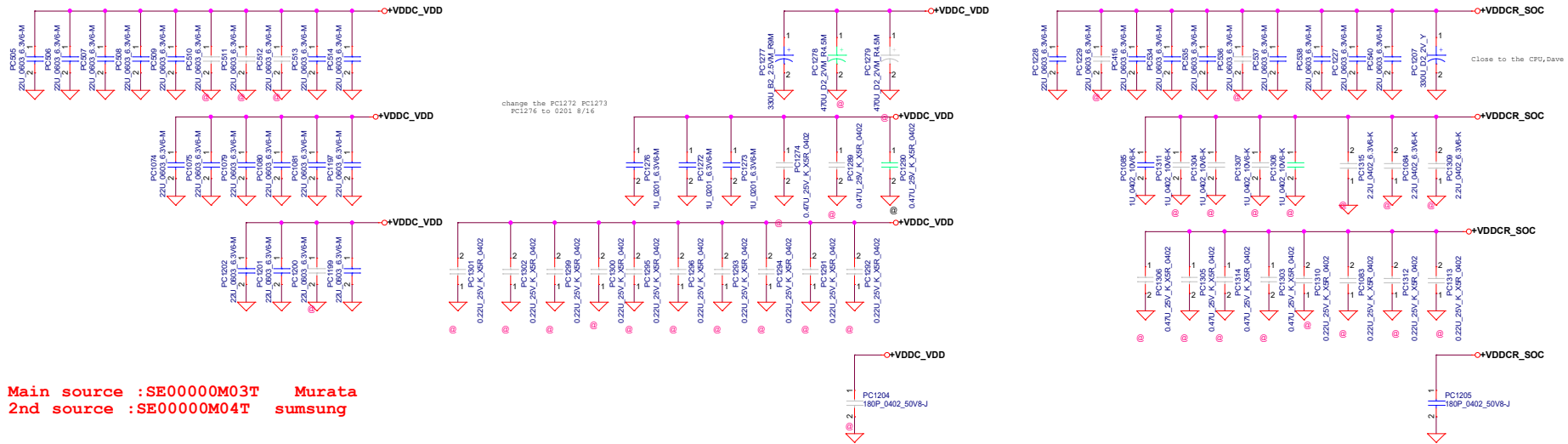


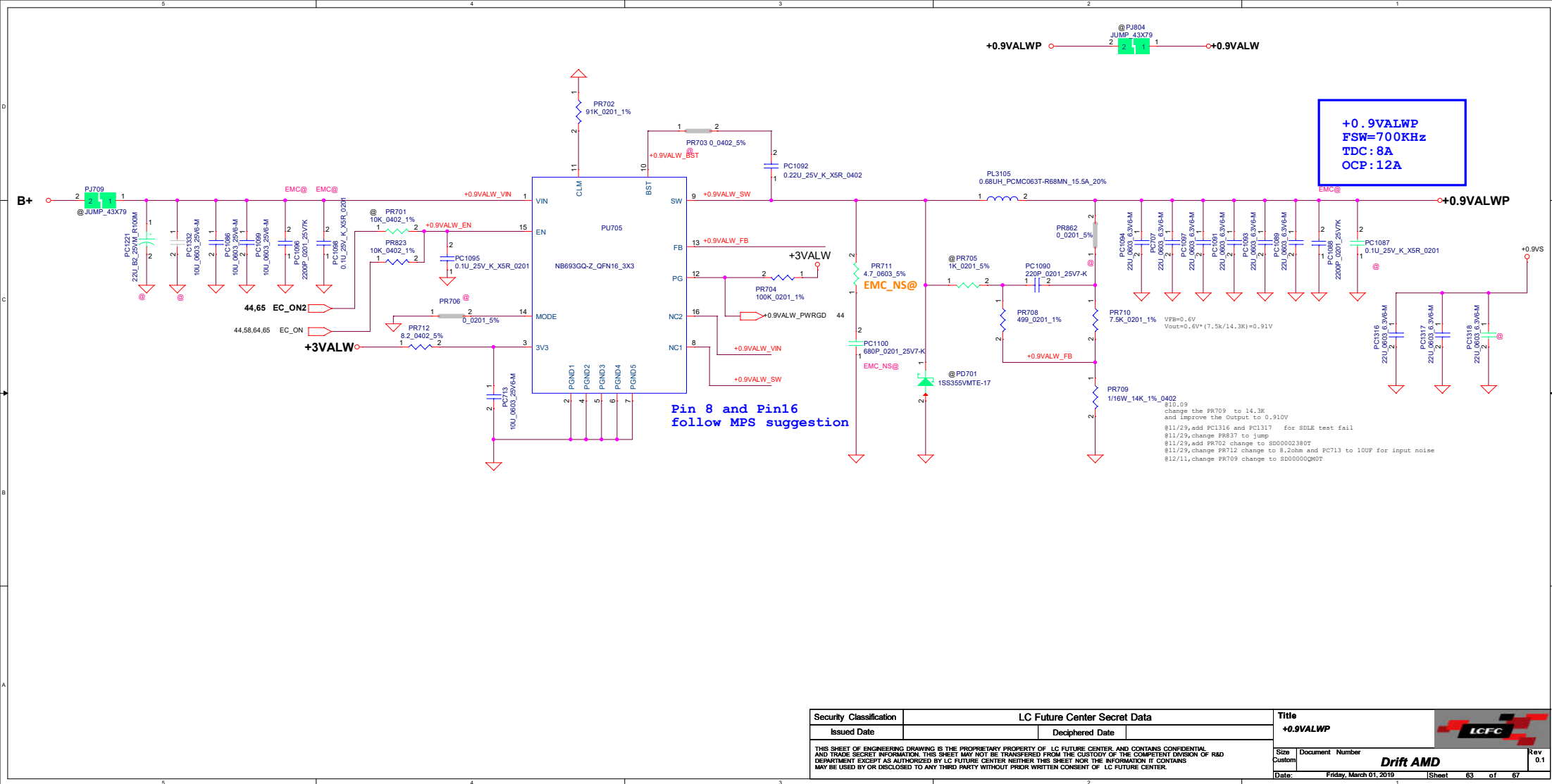
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